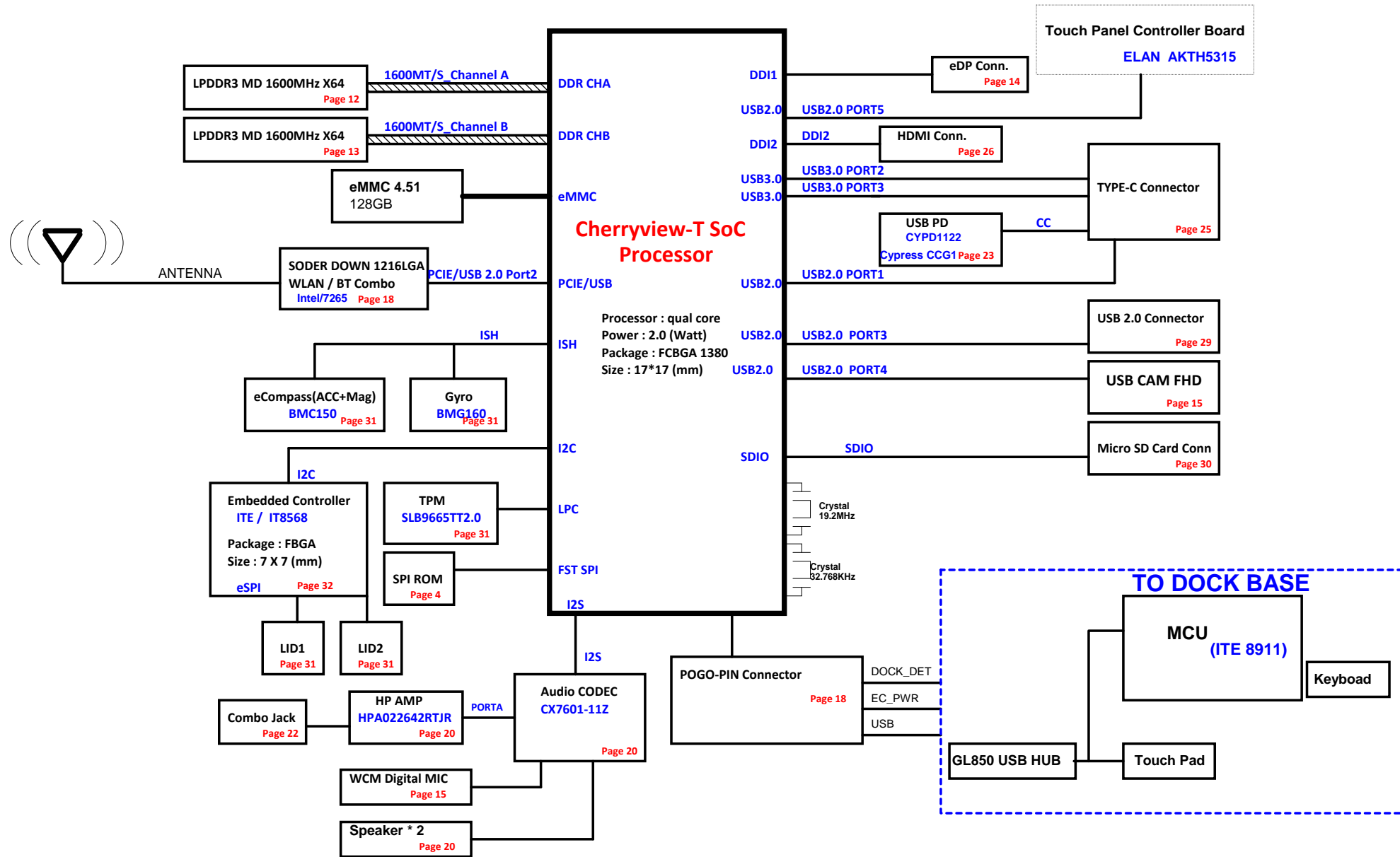
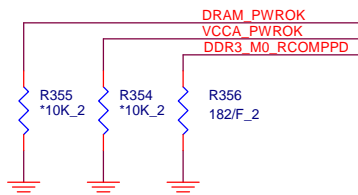
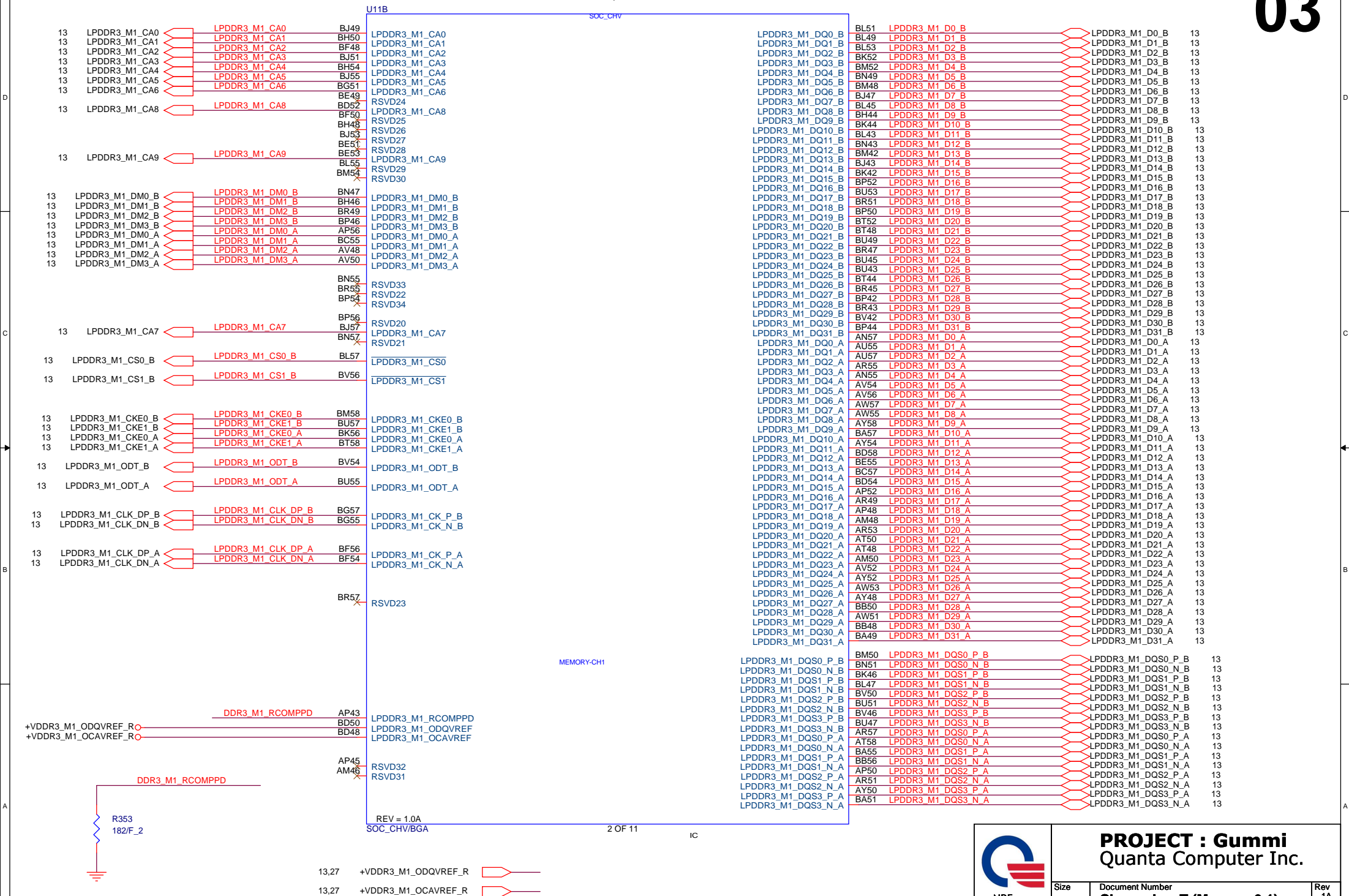


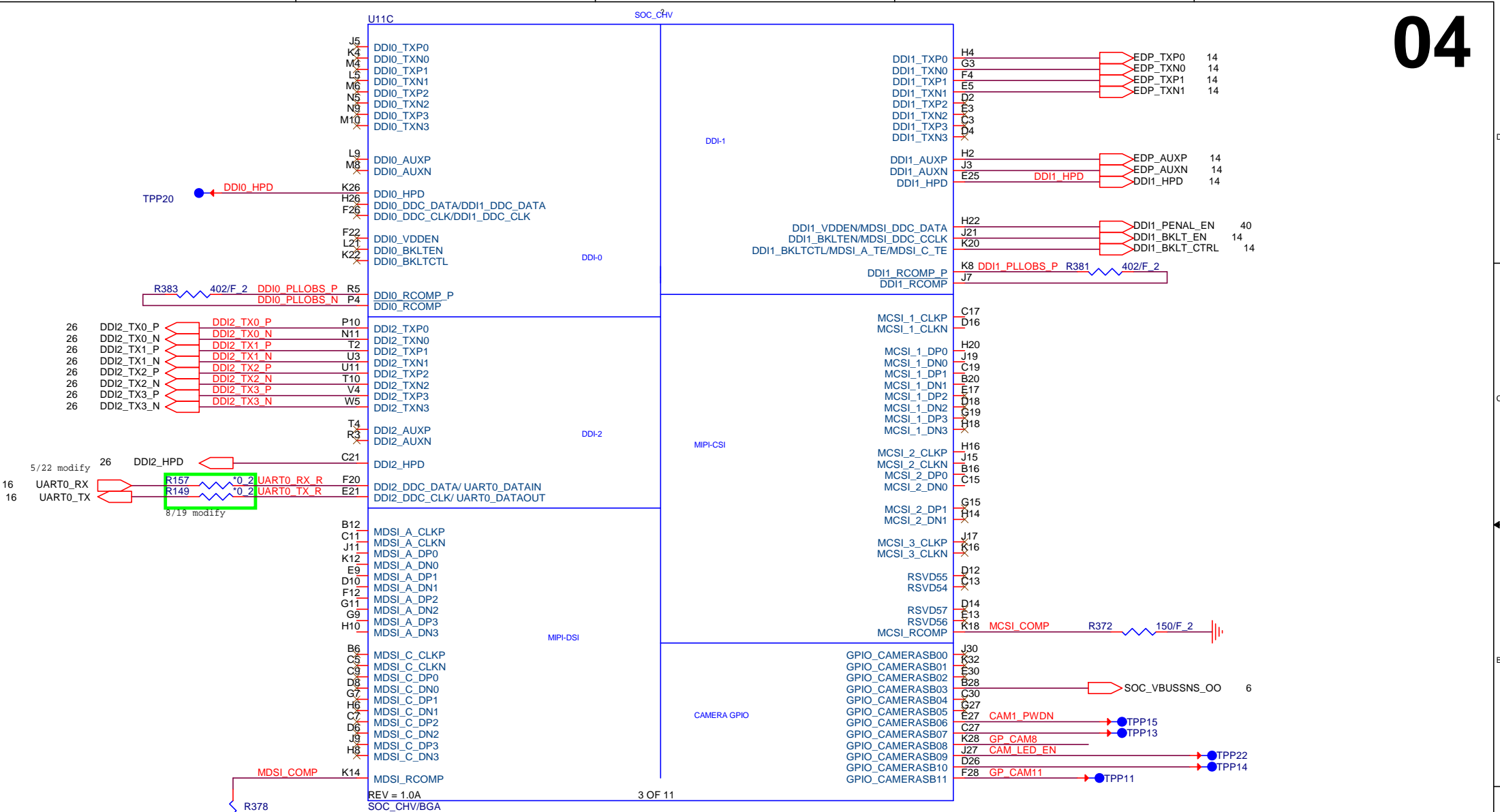
Gummi Cherry Trail Block Diagram

01









Hardware Strap

Pin Name	Purpose	Polarity	Internal PU/PD	
GPIO_CAMERAB08	ICLK Xtal OSC Bypass	0 = No Bypass 1 = Bypass	PD	R144 *10K_2 GP_CAM8
GPIO_CAMERAB09	CCU SUS RO Bypass	0 = No Bypass 1 = Bypass	PD	R147 *10K_2 CAM_LED_EN
GPIO_CAMERAB11	RTC OSC Bypass	0 = No Bypass 1 = Bypass	PD	R128 *10K_2 GP_CAM11

PROJECT : Gummi
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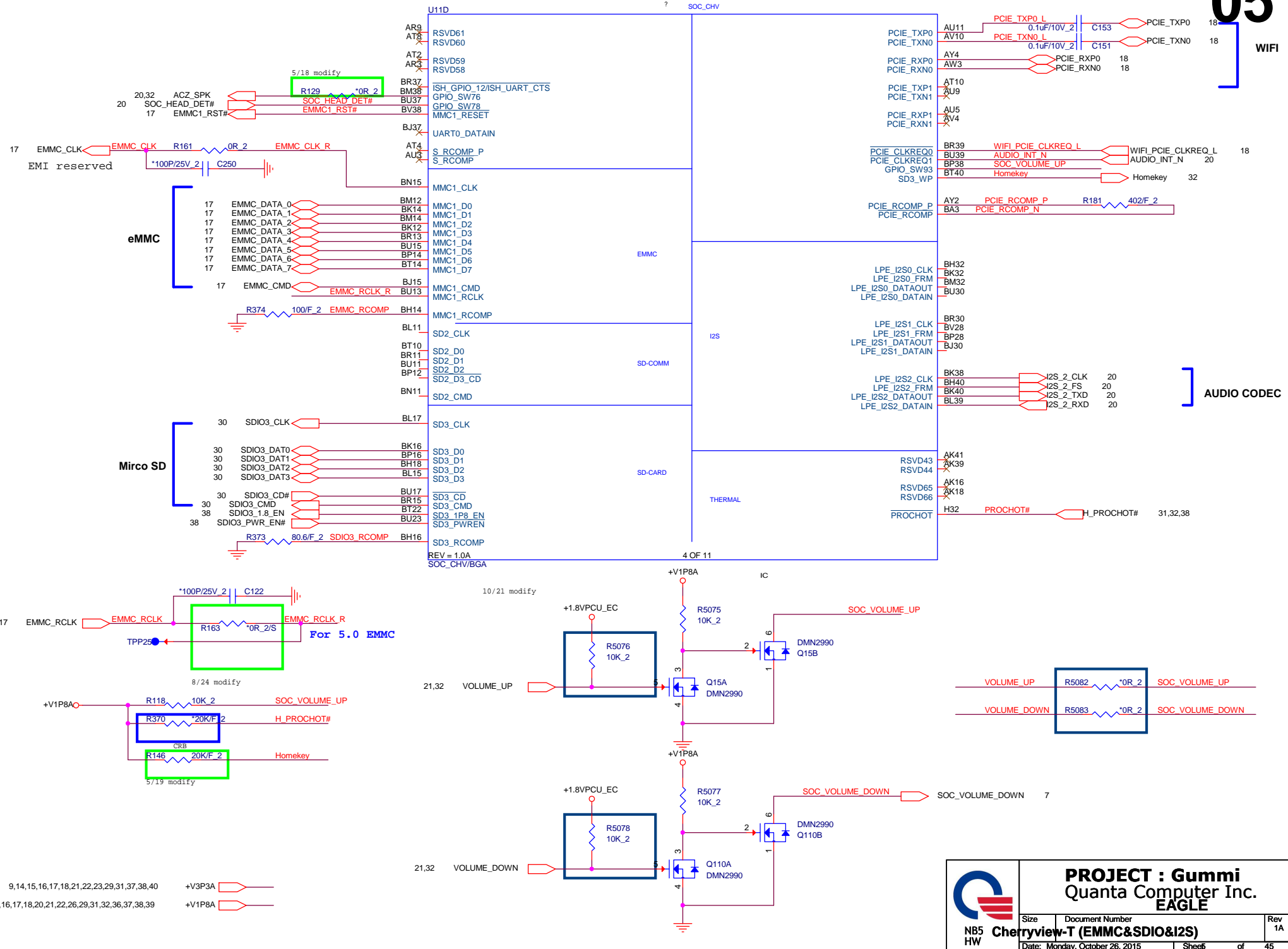
Size: _____

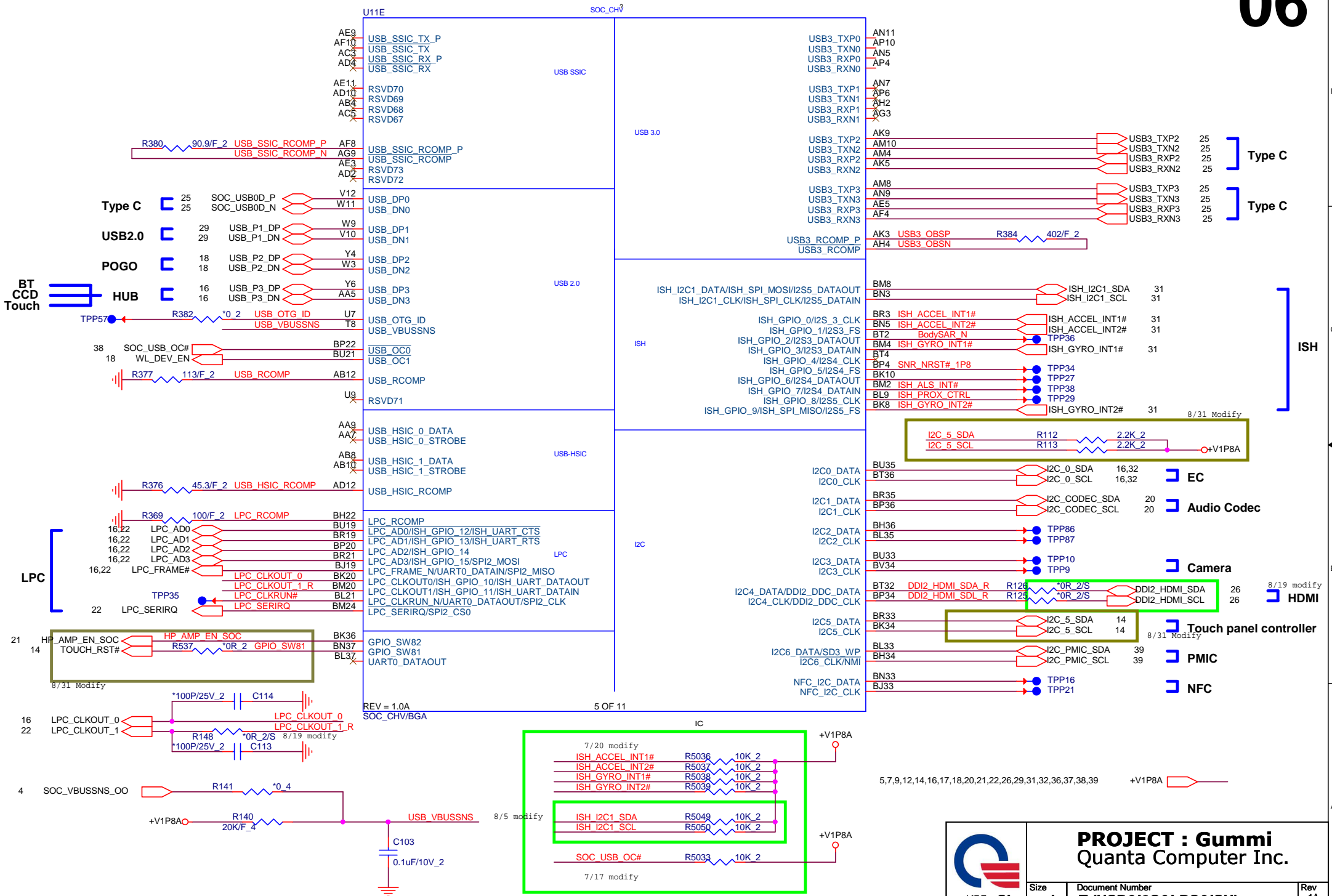
Document Number: **Cherryview-T (DISPLAY/CAM)**

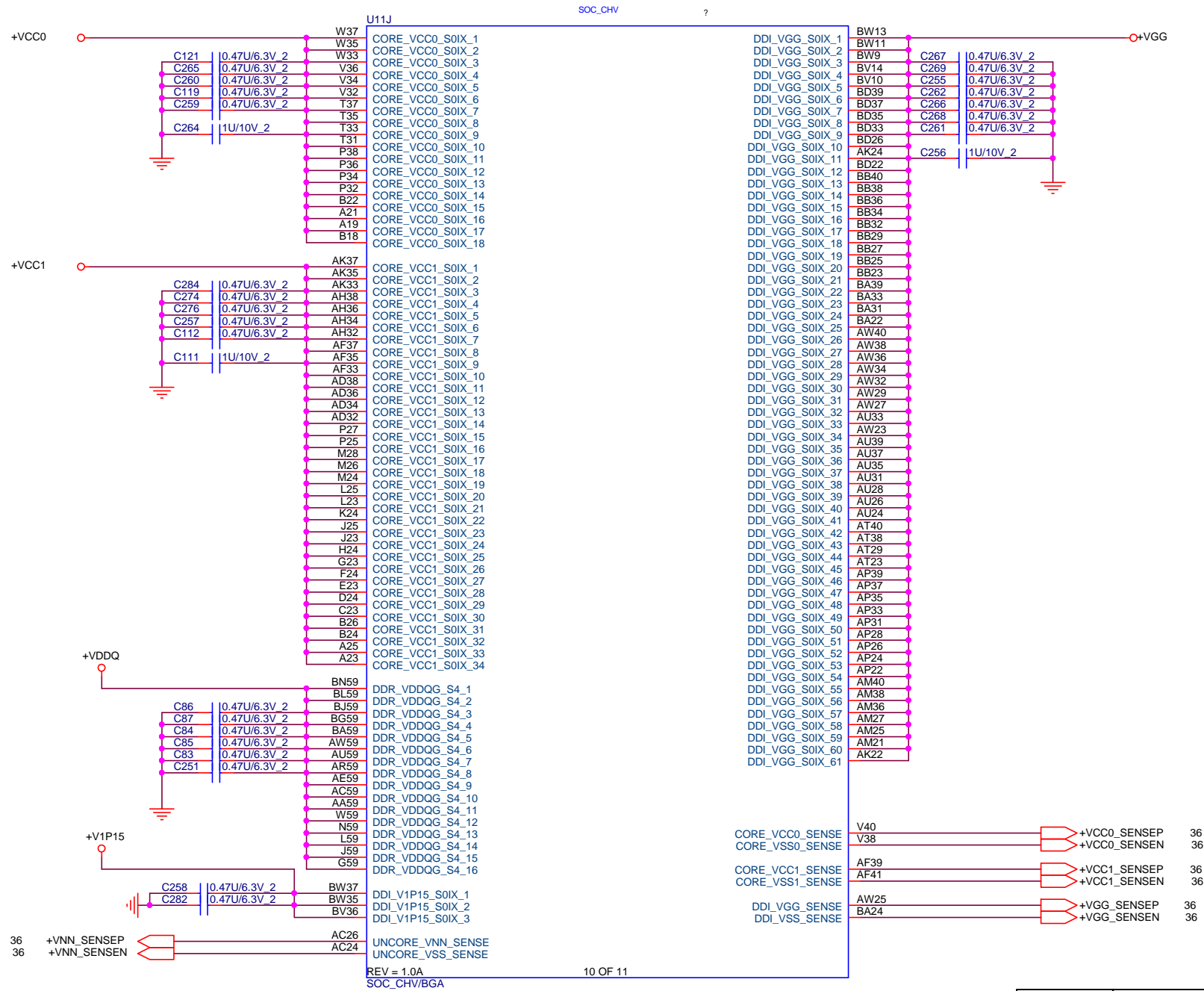
Date: Monday, October 26, 2015


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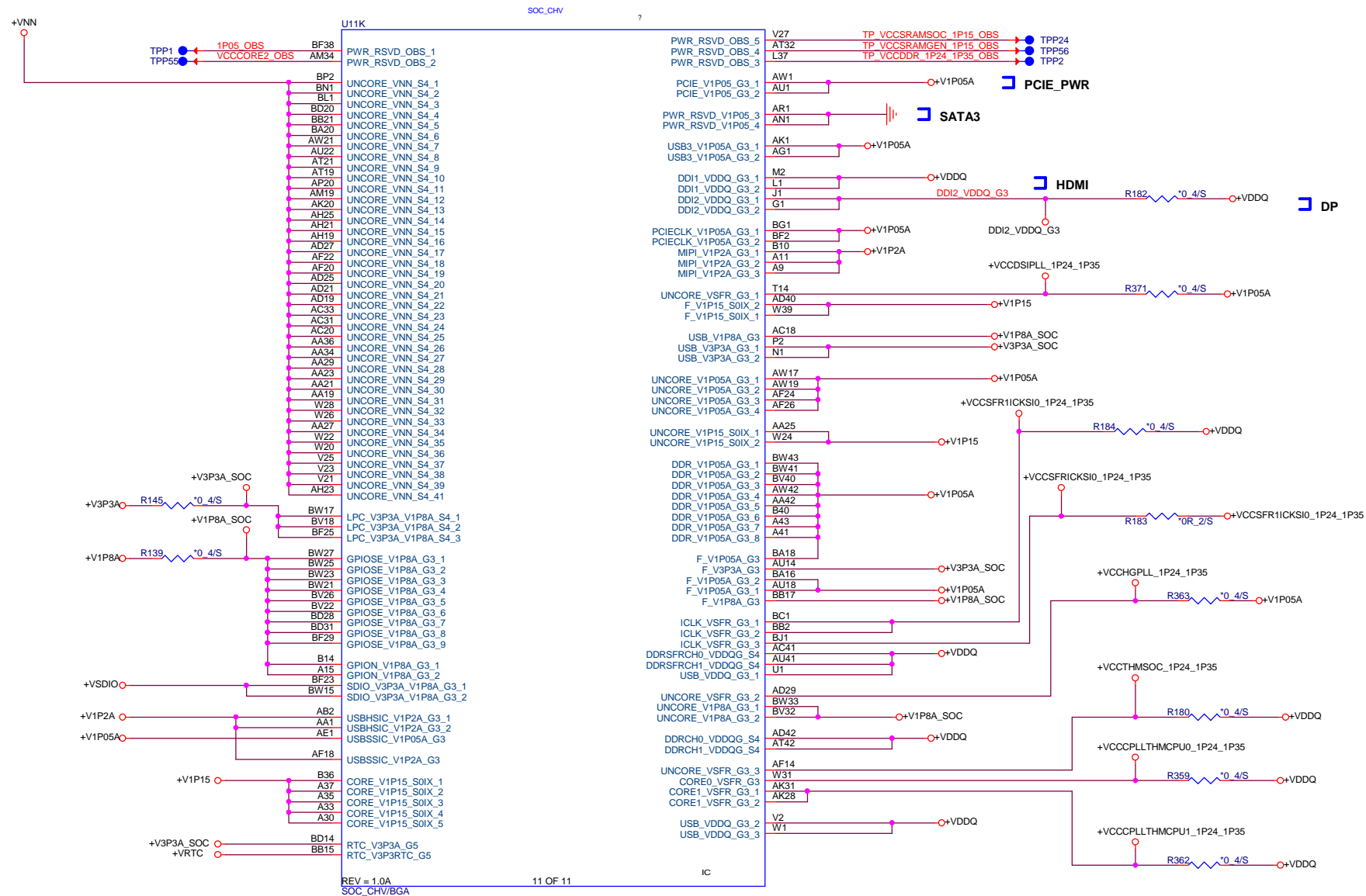




NB5
HW

PROJECT : Gummi
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EAGLE

Size	Document Number	Rev
	Cherryview-T (POWER 1/2)	1A
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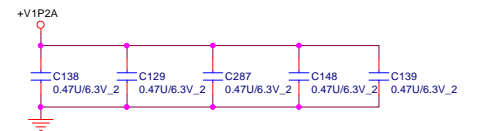
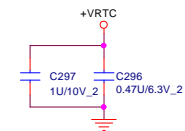
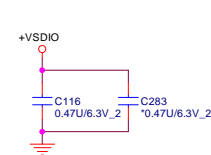
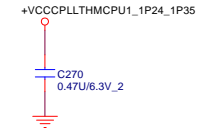
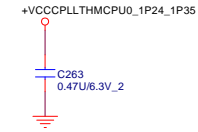
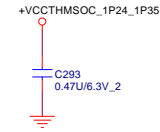
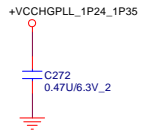
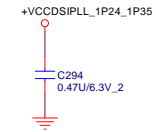
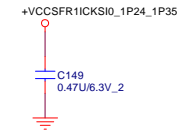
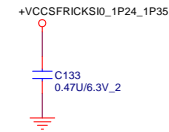
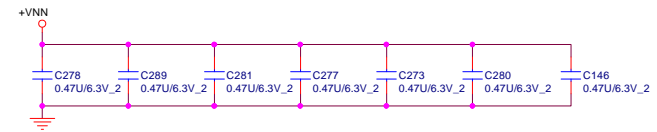
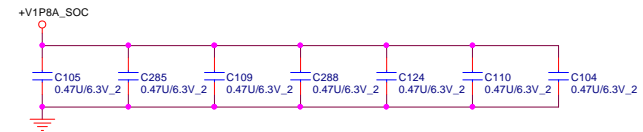
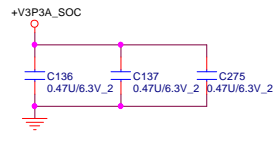
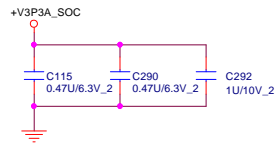
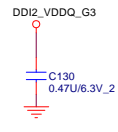
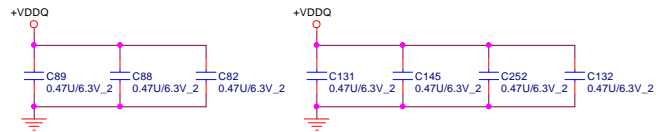
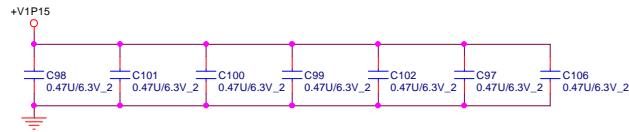
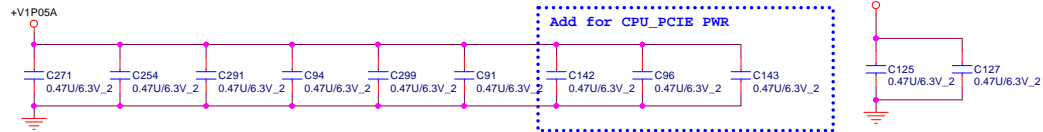
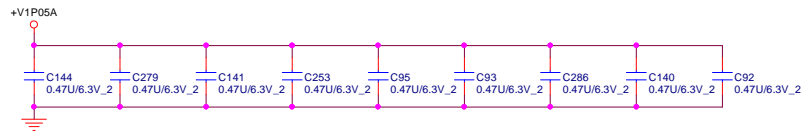


7,11,21,28,37	+VRTC
11,36	+VNN
14,15,16,17,18,21,22,23,29,31,37,38,40	+V3P3A
5,6,7,12,14,16,17,18,20,21,22,26,29,31,32,36,37,38,39	+V1P8A
11,37	+VSDIO
11,37	+V1P2A
11,36,37	+V1P05A
8,11,16,36	+V1P15
8,11,12,13,36,37	+VDDQ

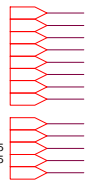


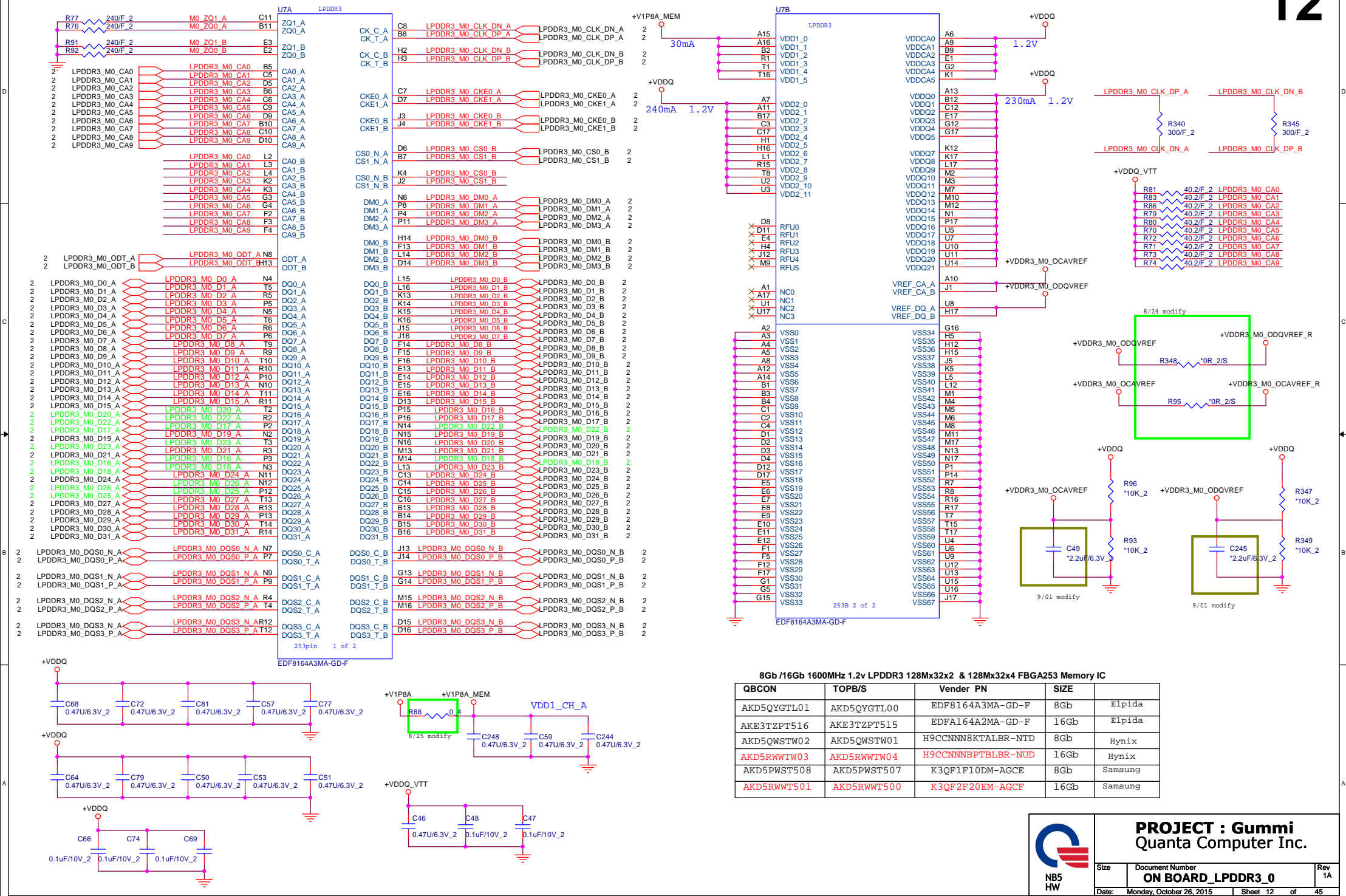
PROJECT : Gummi
Quanta Computer Inc.

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	Cherryview-T (POWER 2/2)	1A
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9,36,37	+V1P05A
8,9,16,36	+V1P15
8,9,12,13,36,37	+VDDQ
9	+V1P8A_SOC
9	+V3P3A_SOC
9,37	+V1P2A
7,9,21,28,37	+VRTC
9	+VCCSFR1ICKSI0_1P24_1P35
9	+VCCDSIPLL_1P24_1P35
9	+VCCTHMSOC_1P24_1P35
9	+VCCCPLLTHMCPU0_1P24_1P35
9	+VCCCPLLTHMCPU1_1P24_1P35
9	+VCCHGPLL_1P24_1P35



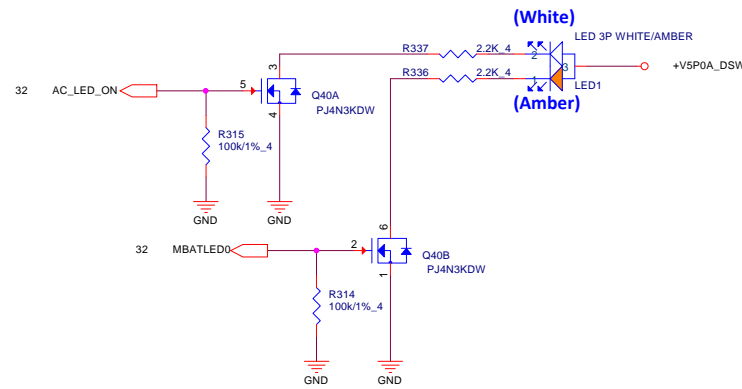
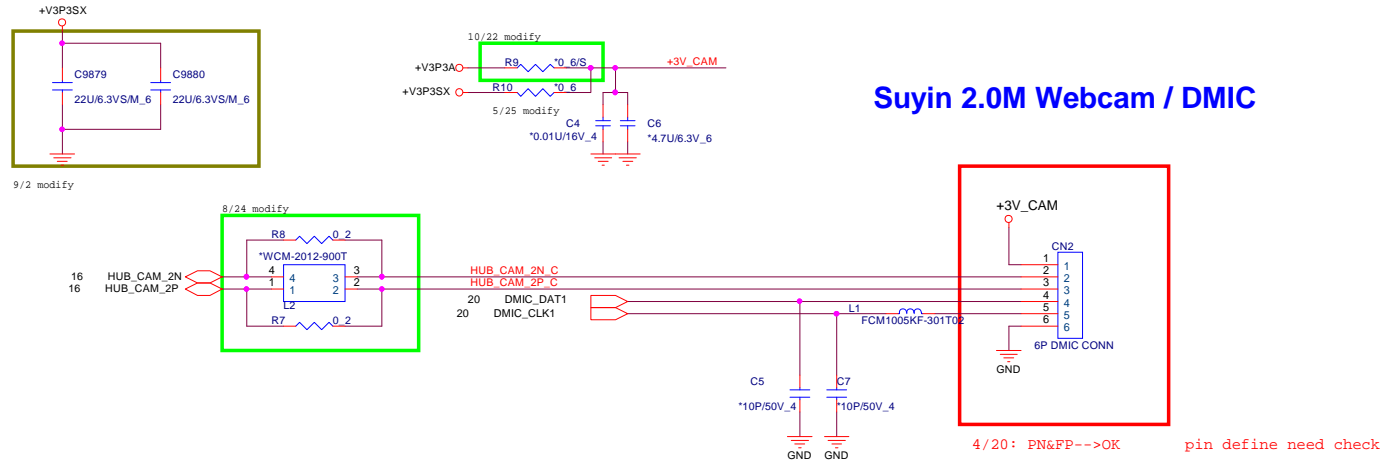


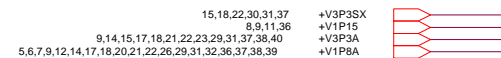
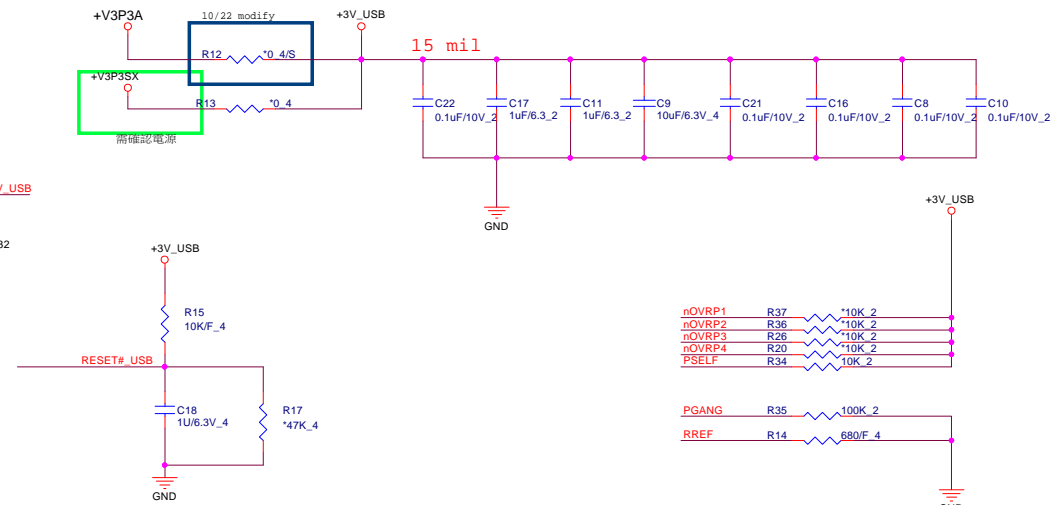


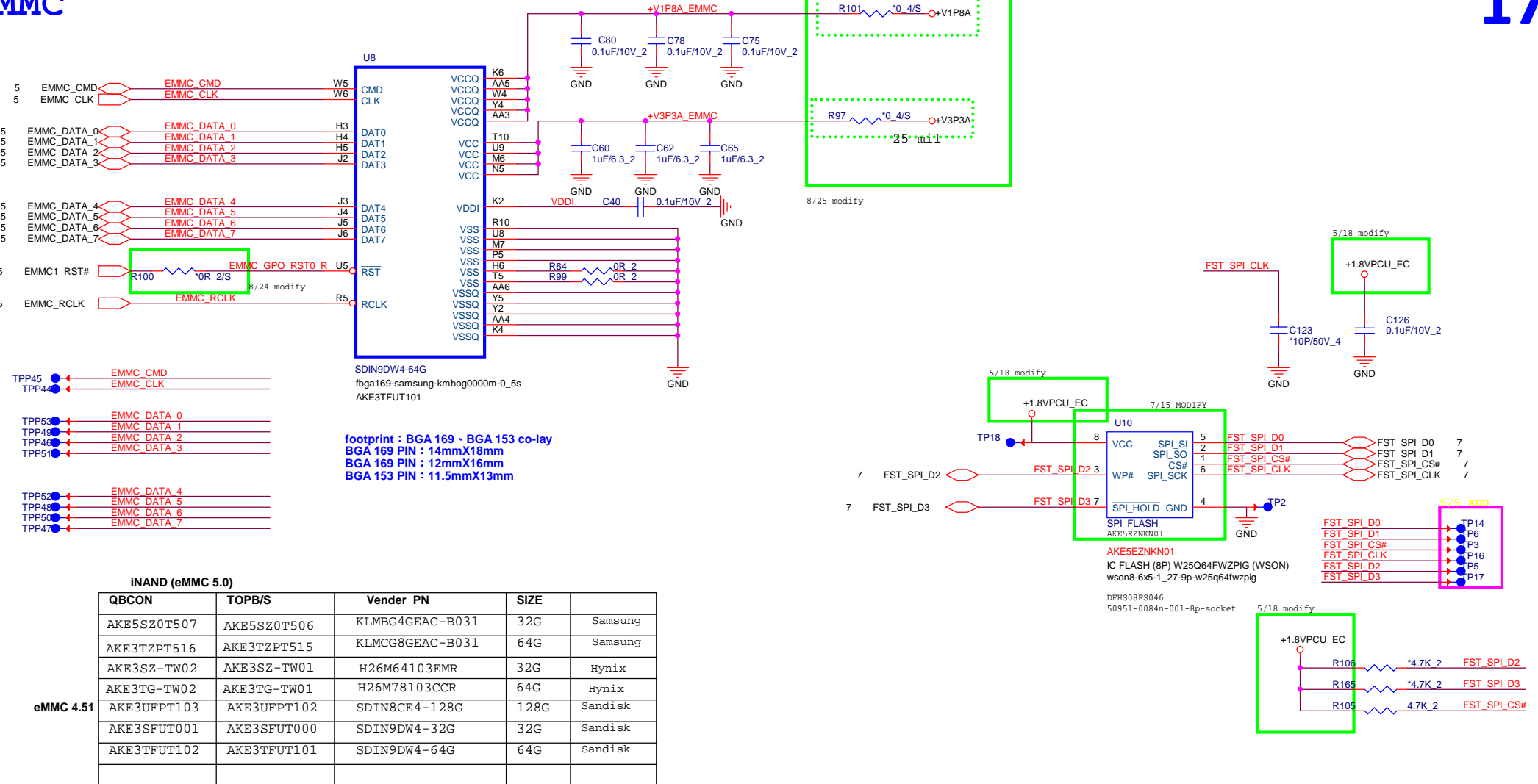


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Quanta Computer Inc.

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	eDP	1A
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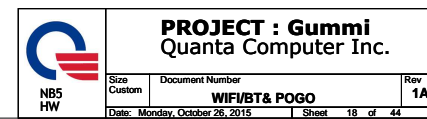
PROJECT : Gummi Quanta Computer Inc.

Size	Document Number	Rev
	eMMC/SPI ROM	1A
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9,14,15,16,18,21,22,23,29,31,37,38,40
5,6,7,9,12,14,16,18,20,21,22,26,29,31,32,36,37,38,39

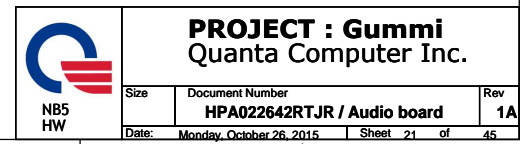
+V3P3A
+V1P8A



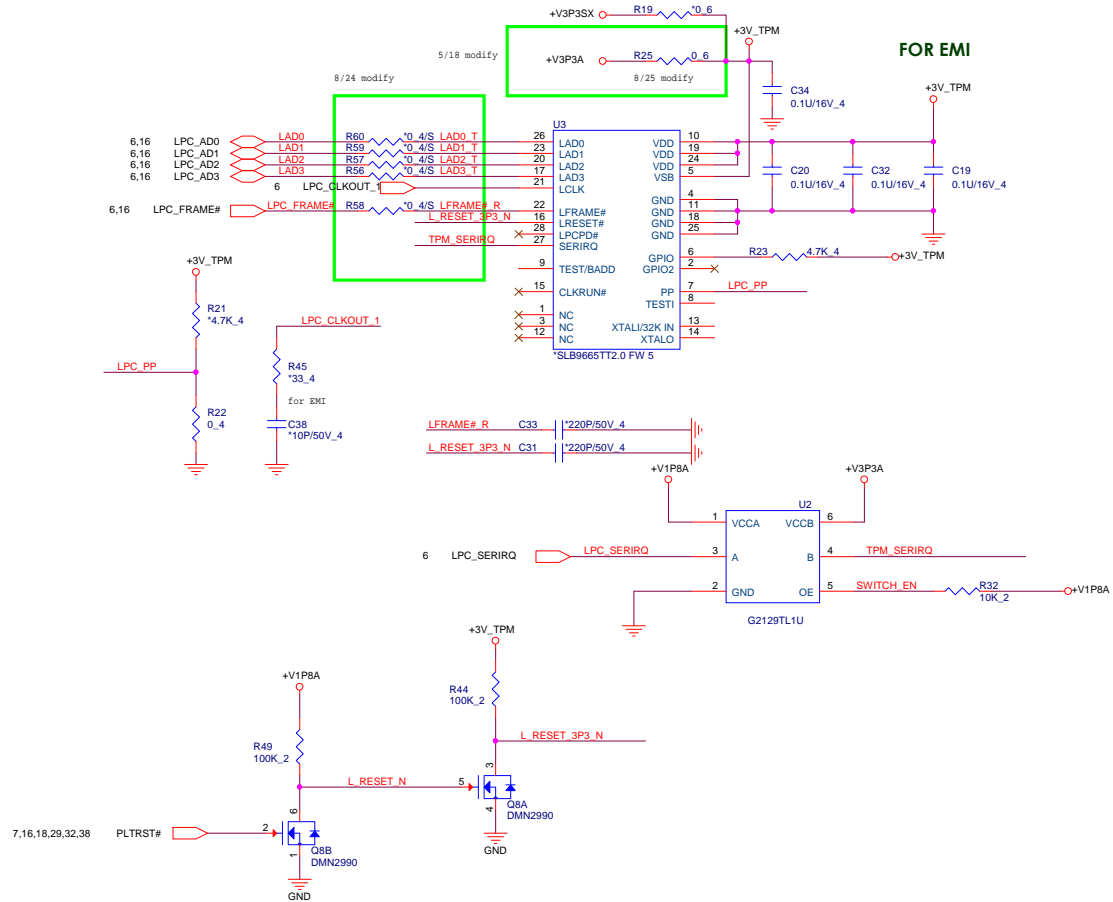




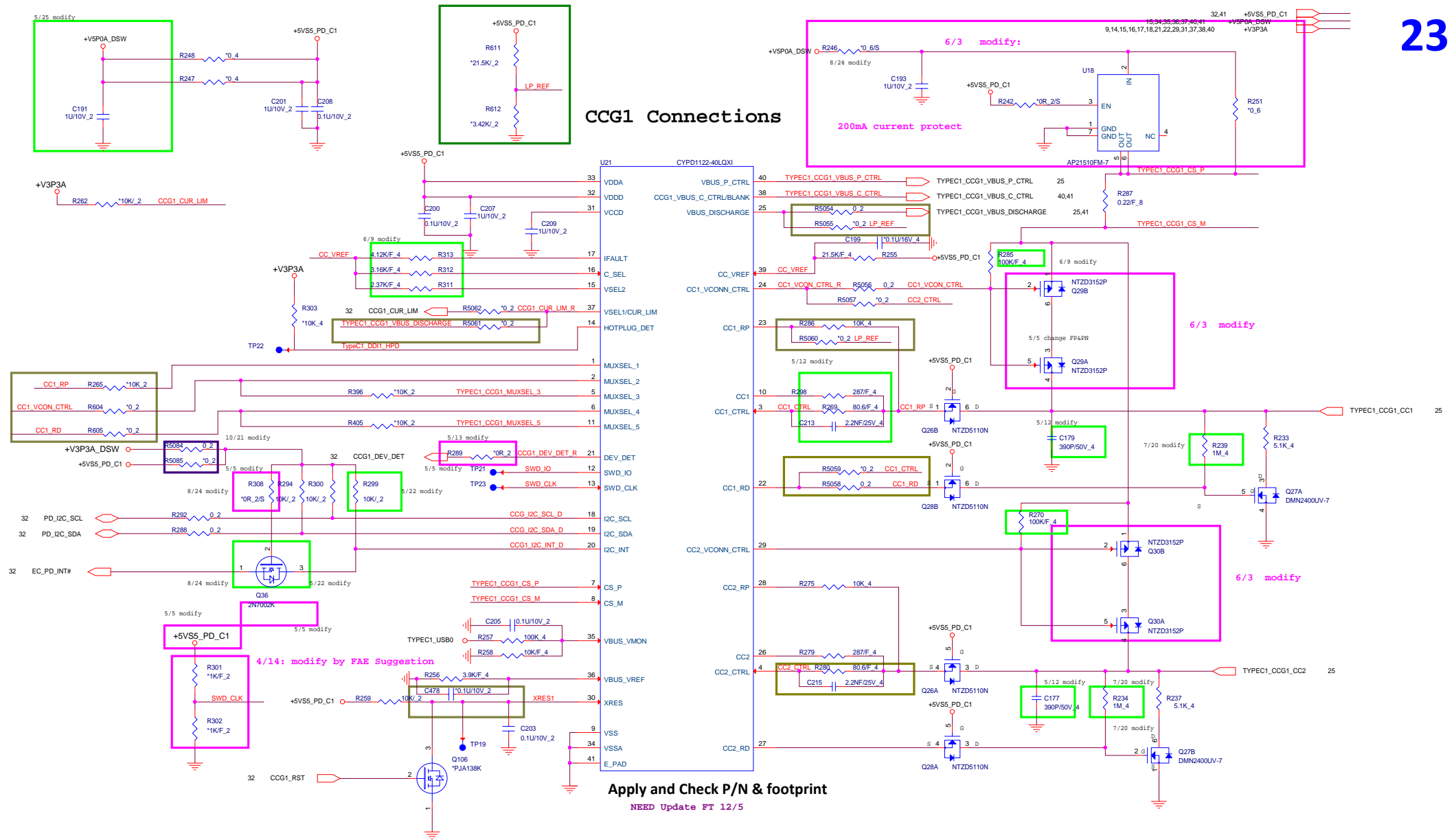
21

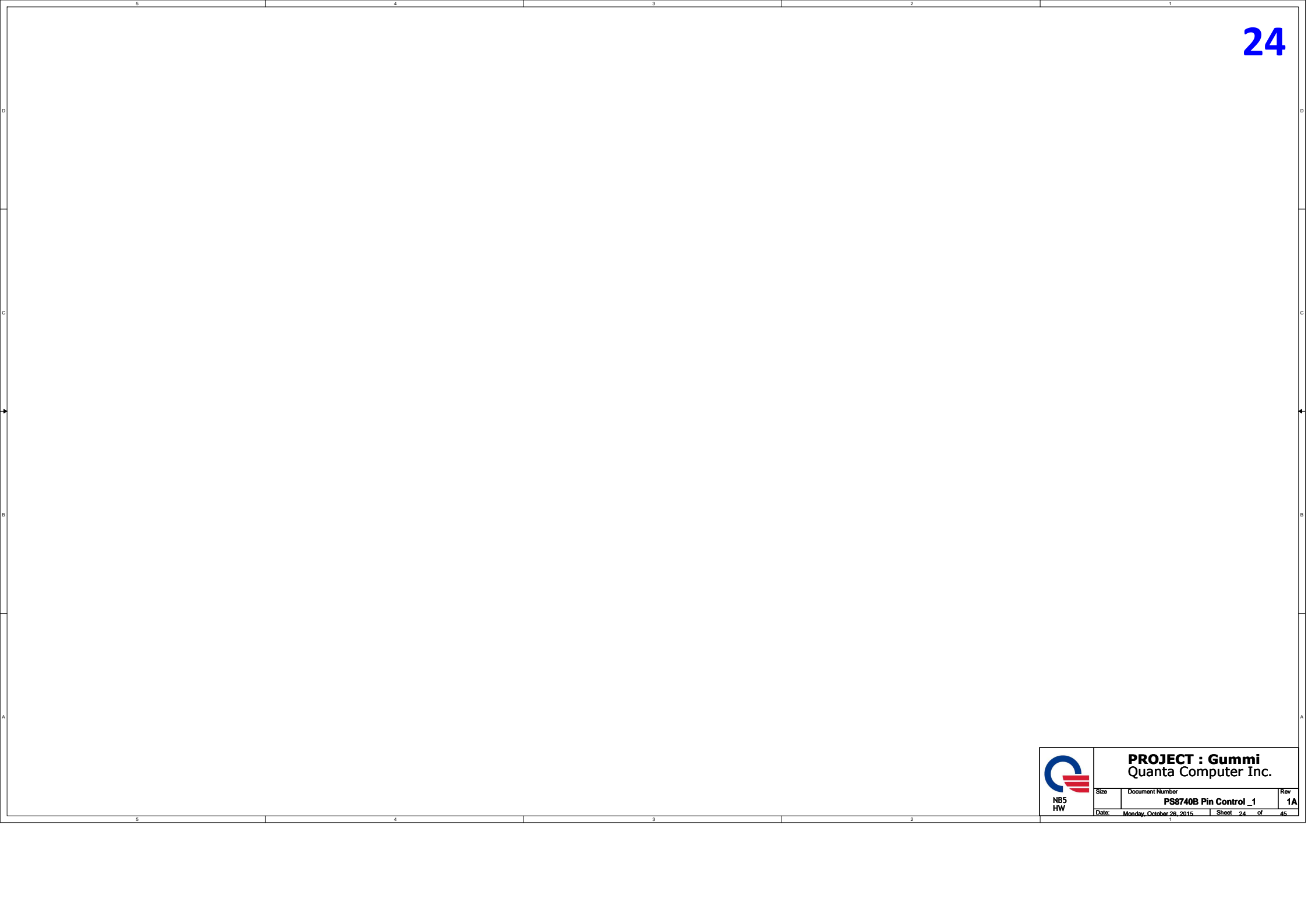


TPM (2.0)



CCG1 Connections





PROJECT : Gummi Quanta Computer Inc.		
Size	Document Number	Rev
	PS8740B Pin Control _1	1A
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SOC_USB0D_N
SOC_USB0D_P

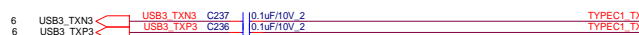
SOC_USB0D_N_C
SOC_USB0D_P_C

"DLP11SN900HL2L"
R325 "OR 2/S"

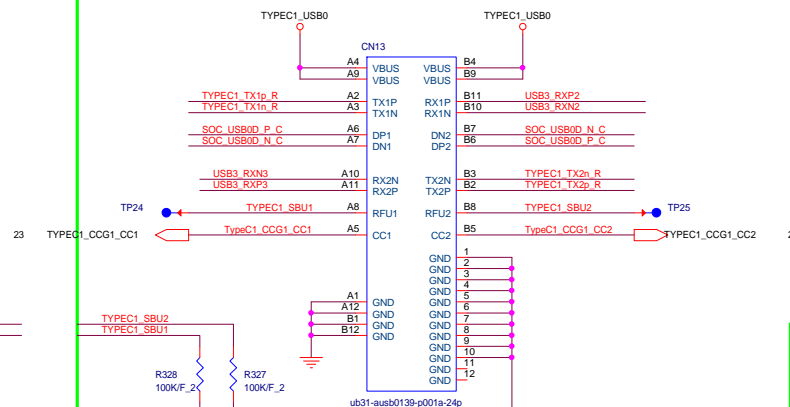
USB3.0 PORT2



USB3.0 PORT3

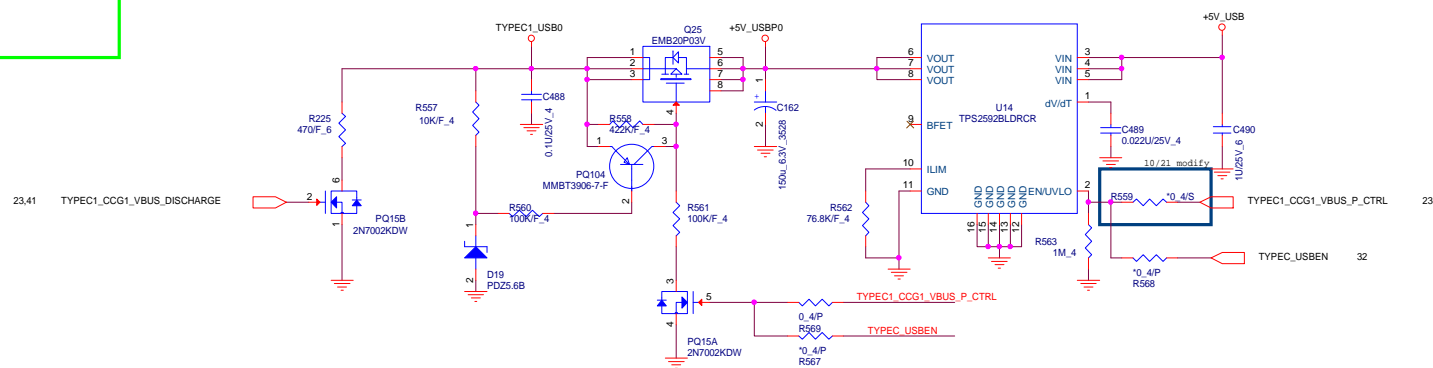
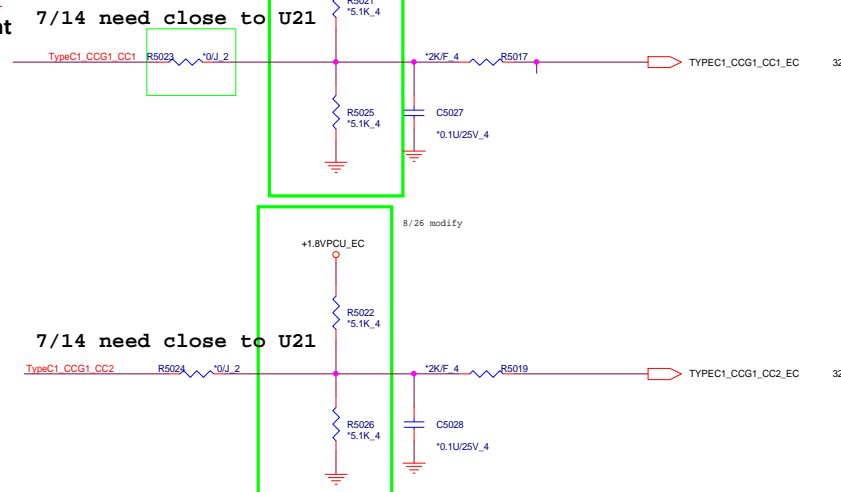


8/24 modify



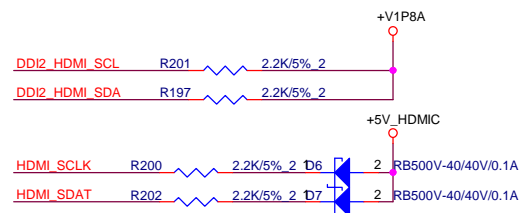
Check P/N & footprint 7/14 need close to U21

5/12: Modify



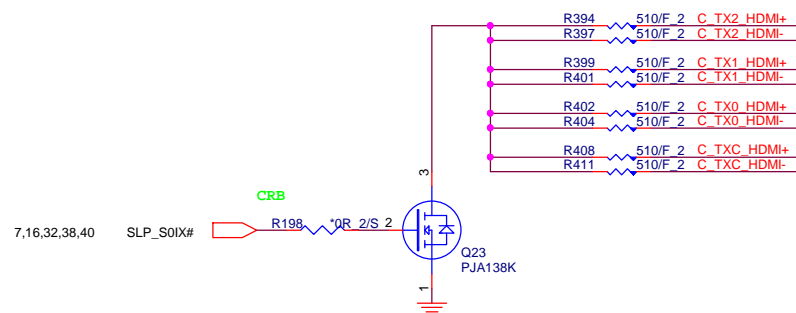
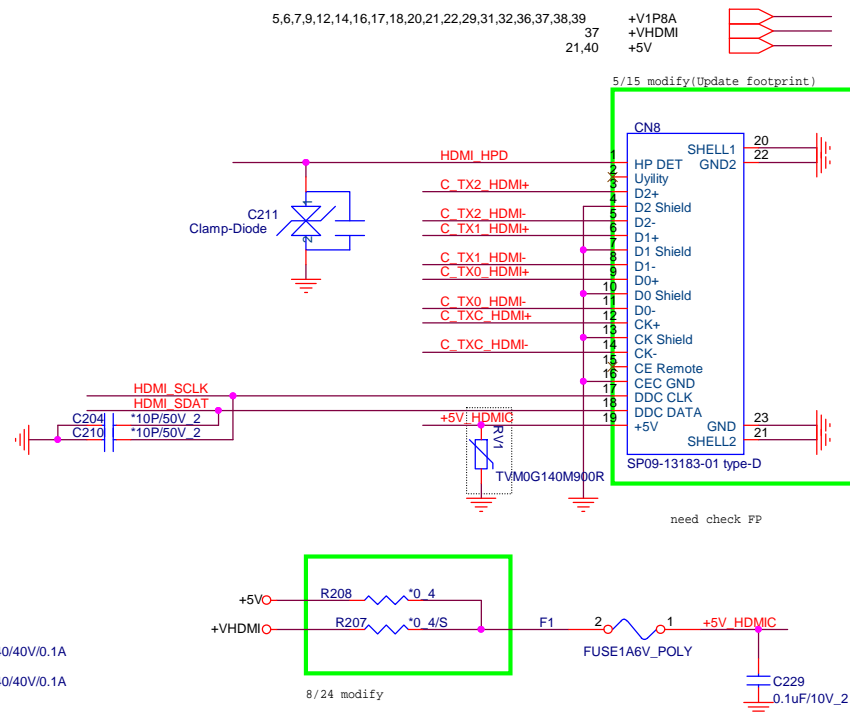


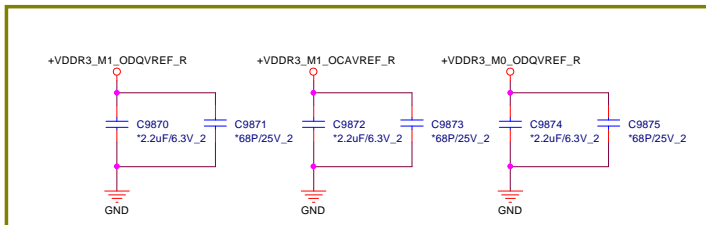
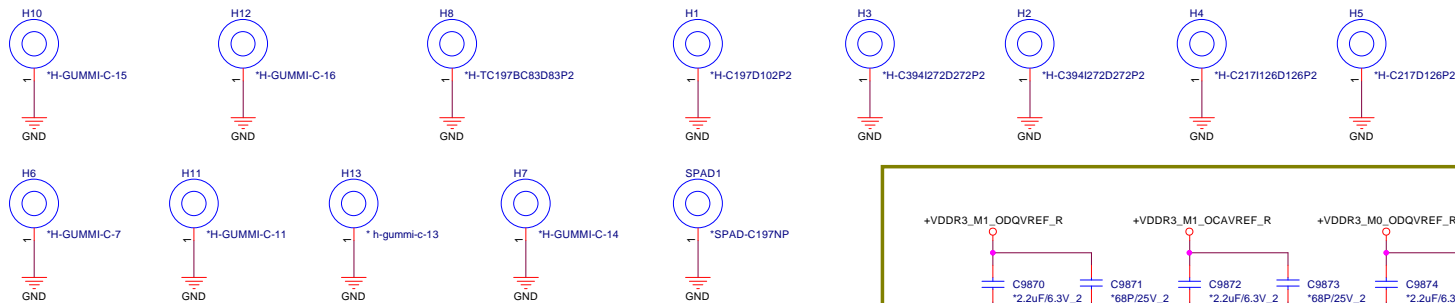
close to HDMI Conn



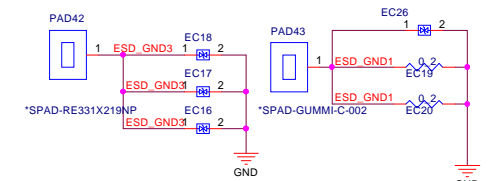
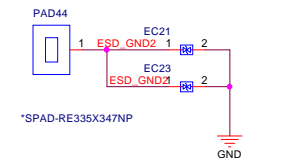
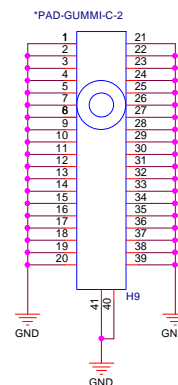
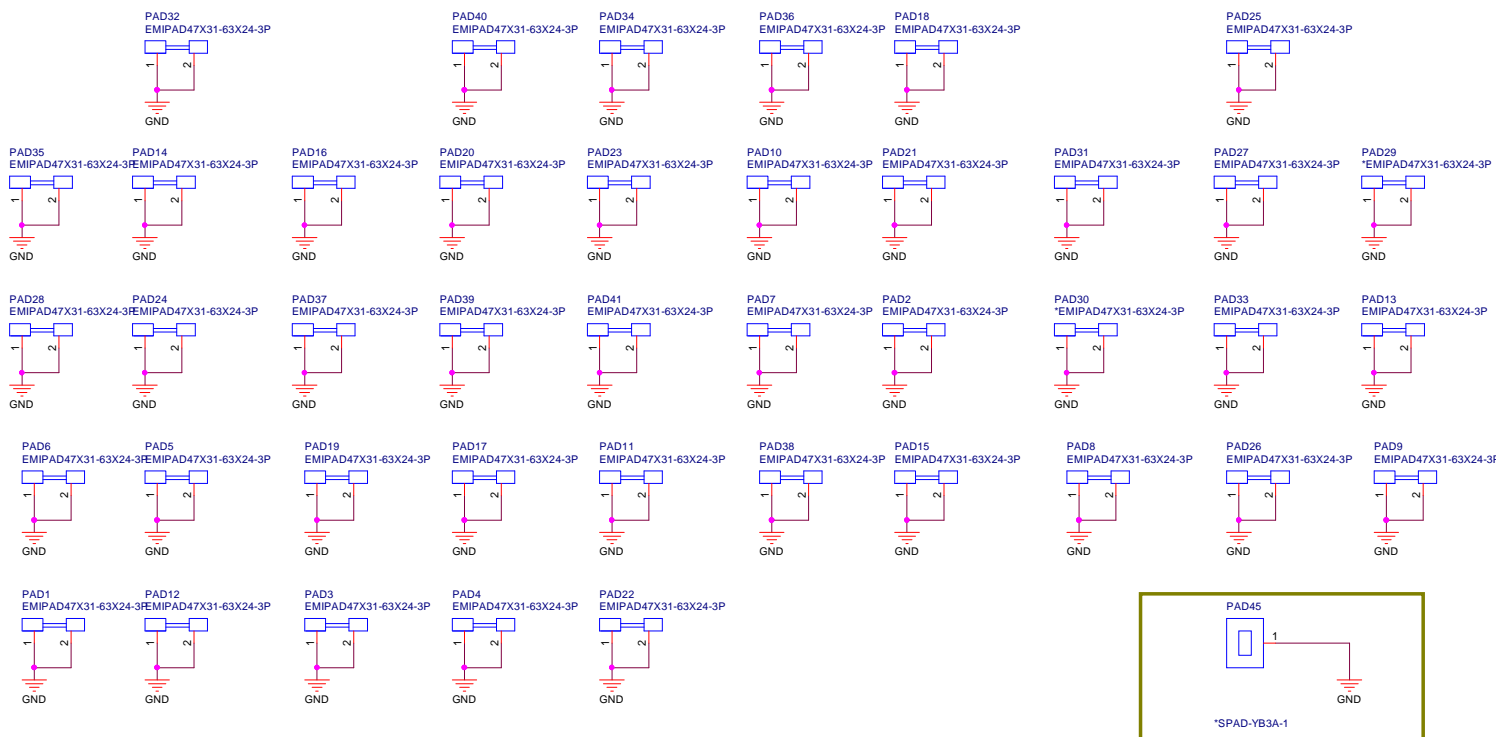
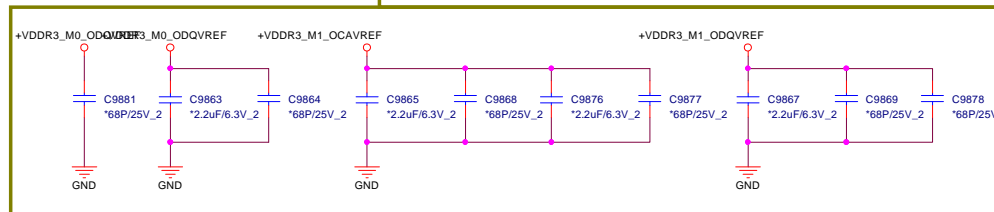
C TX2_HDMI+	R395	180/F 2	C TX2_HDMI-
C TX1_HDMI+	R400	180/F 2	C TX1_HDMI-
C TX0_HDMI+	R403	180/F 2	C TX0_HDMI-
C TXC_HDMI+	R409	180/F 2	C TXC_HDMI-

10/21 modify

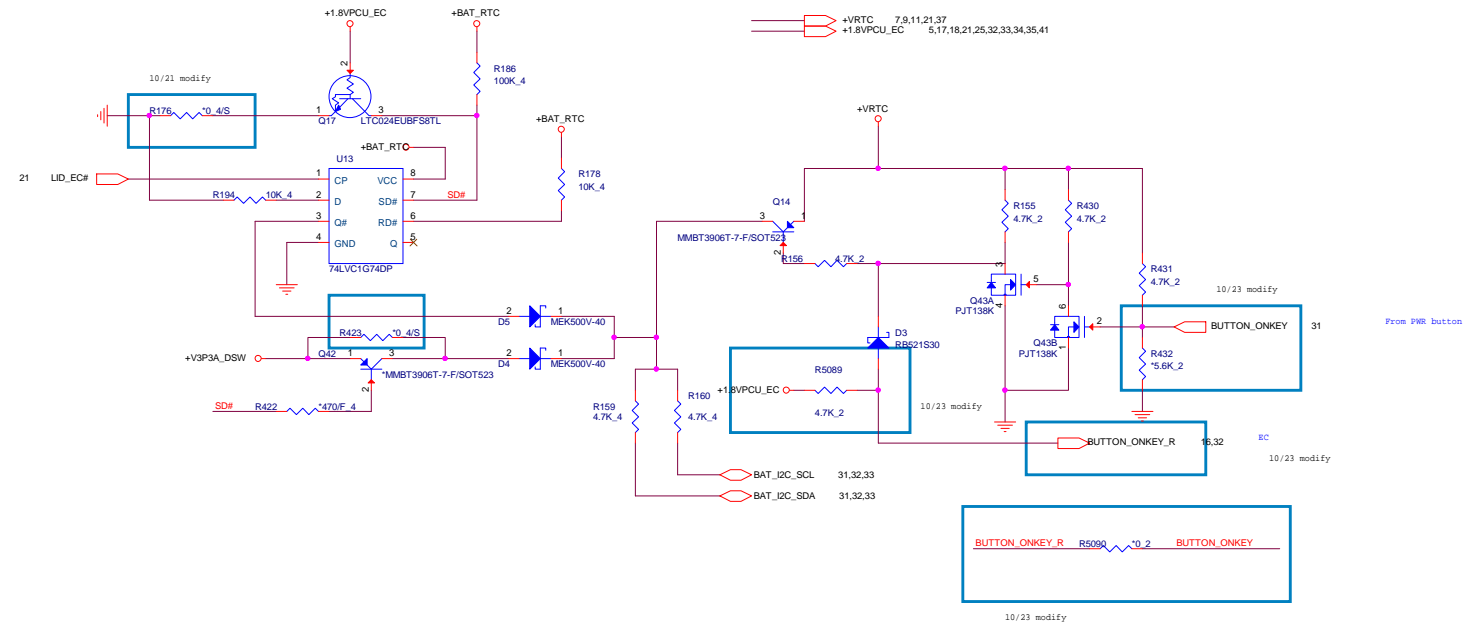




9/01modify



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	Quanta Computer Inc.		
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Input				Output	
SD	RD	CP	D	Q	Q̄
L	H	X	X	H	L
H	L	X	X	L	H
L	L	X	X	H	H

[1] H = HIGH voltage level;
L = LOW voltage level;
X = don't care.

Input				Output	
SD	RD	CP	D	Q _{n+1}	Q̄ _{n+1}
H	H	↑	L	L	H
H	H	↑	H	H	L

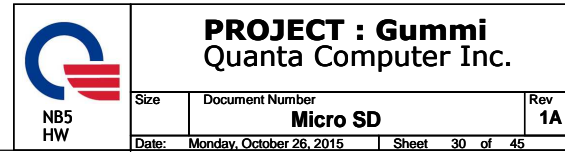
[1] H = HIGH voltage level;
L = LOW voltage level;
↑ = LOW-to-HIGH CP transition;
Q_{n+1} = state after the next LOW-to-HIGH CP transition.



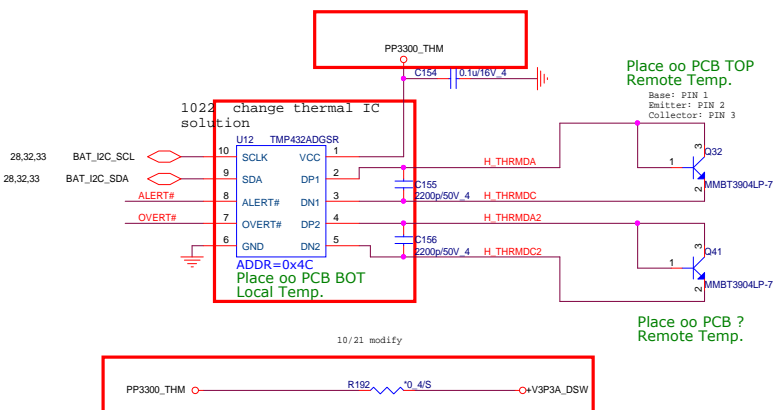
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Quanta Computer Inc.

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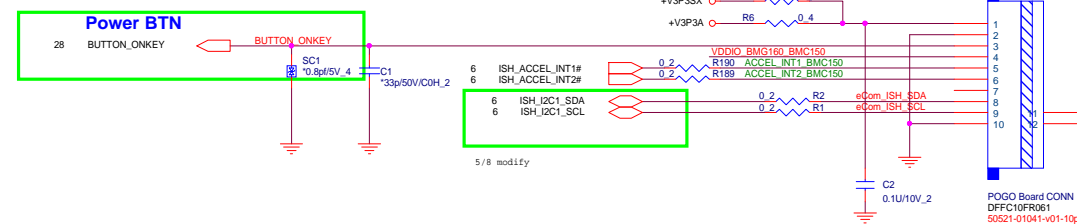
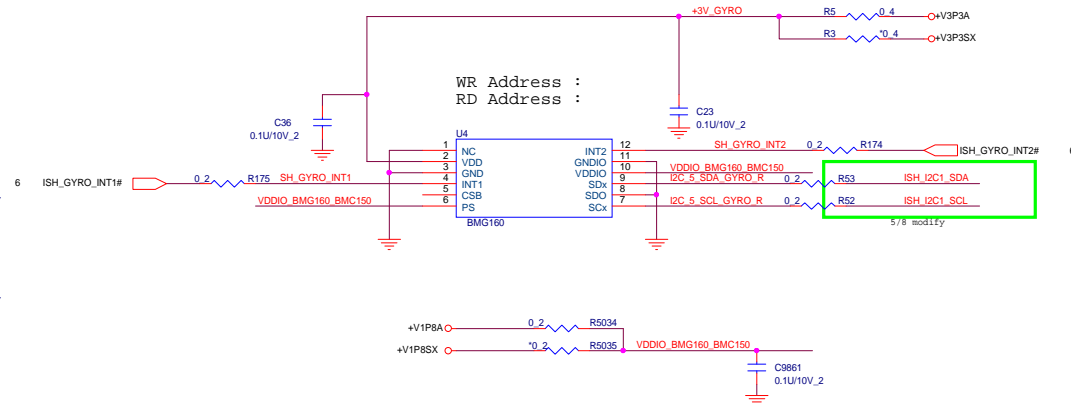




Thermal Sensor(THM)



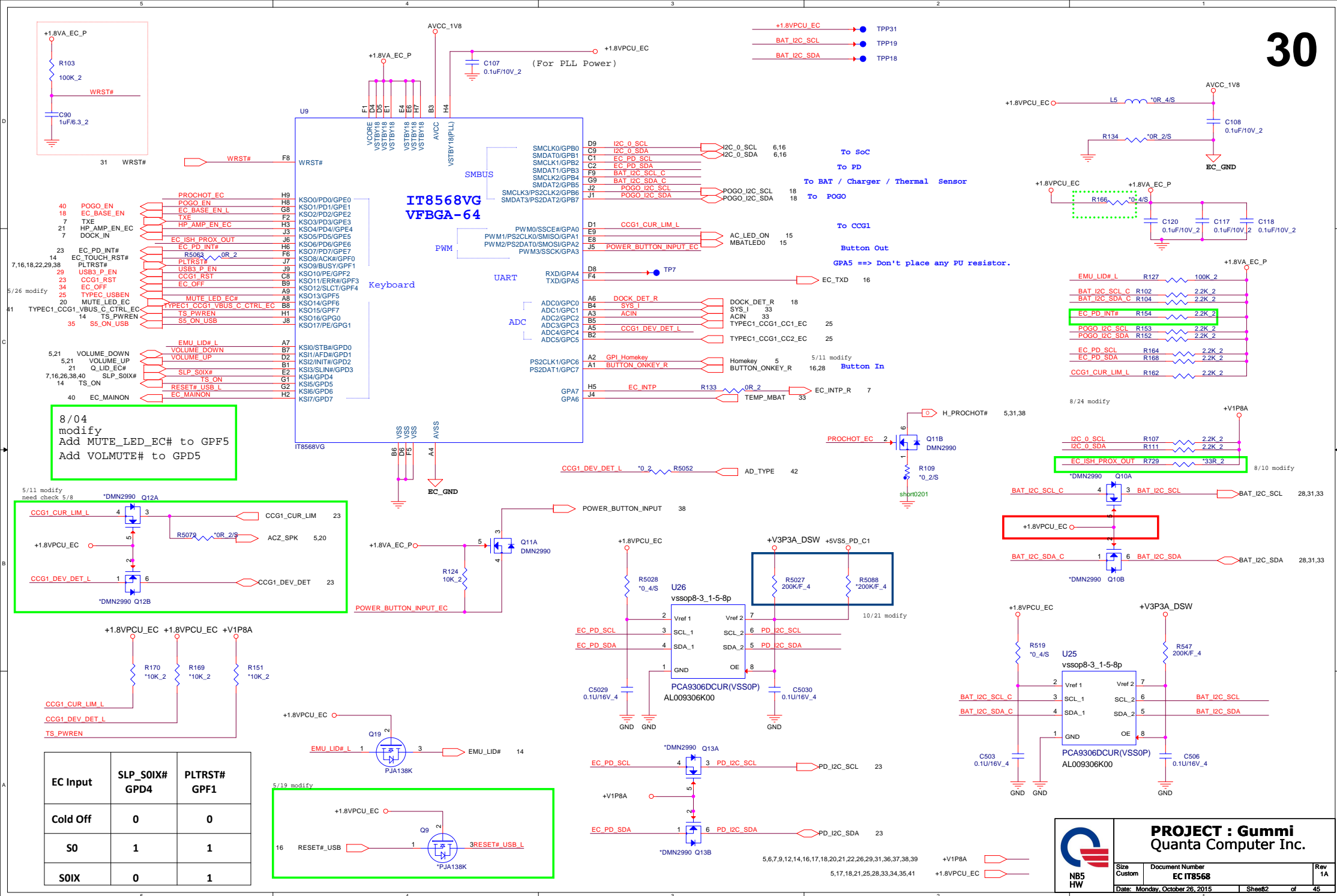
Gyroscope (BMG160)

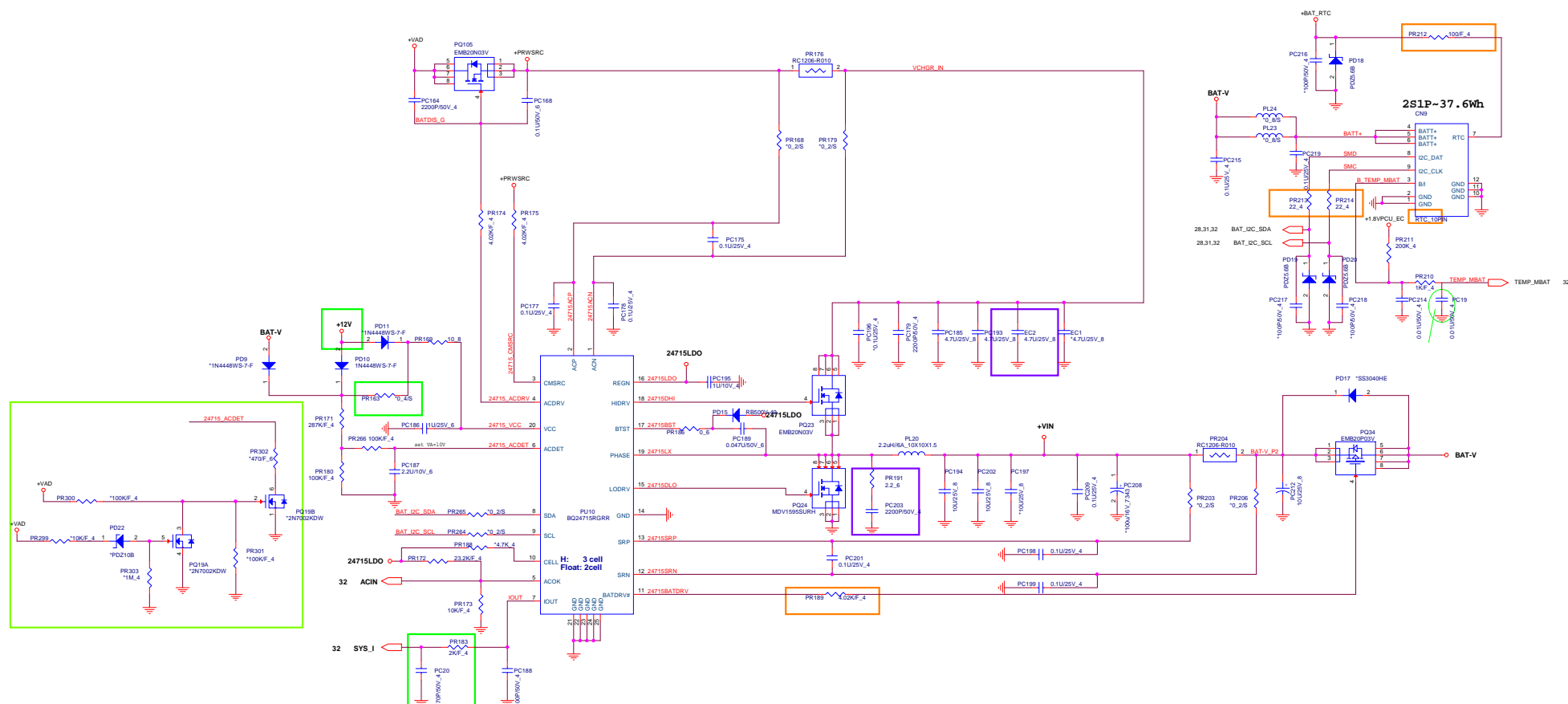


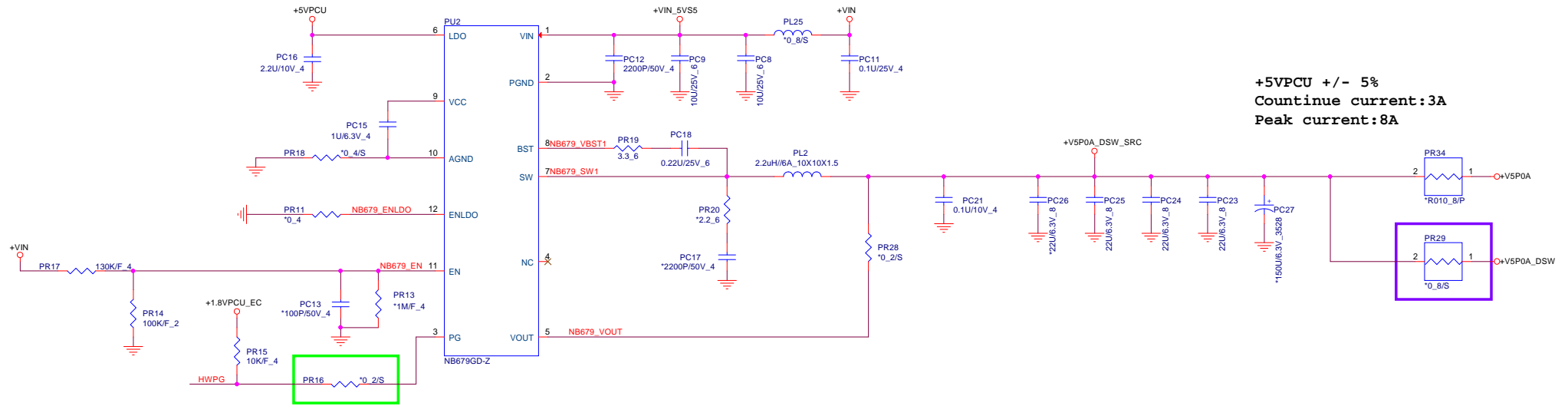
E-compass/Magnetometer/Accelerometer (BMC150)

Keepout area is around 10mm

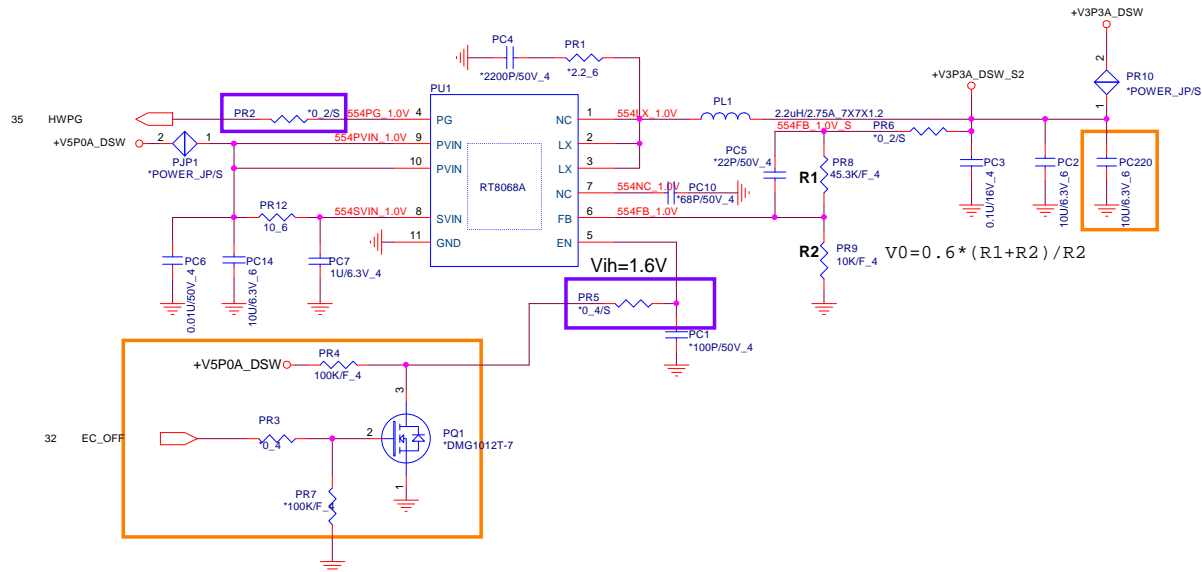
WR Address : 0x3C
RD Address : 0x3D

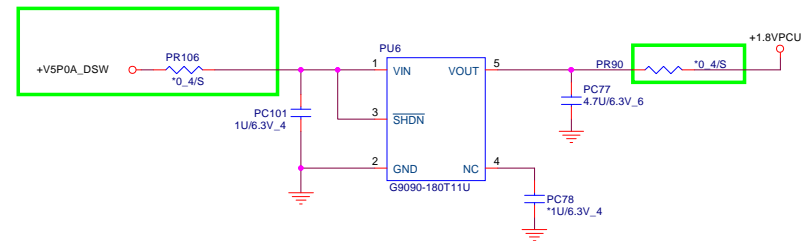


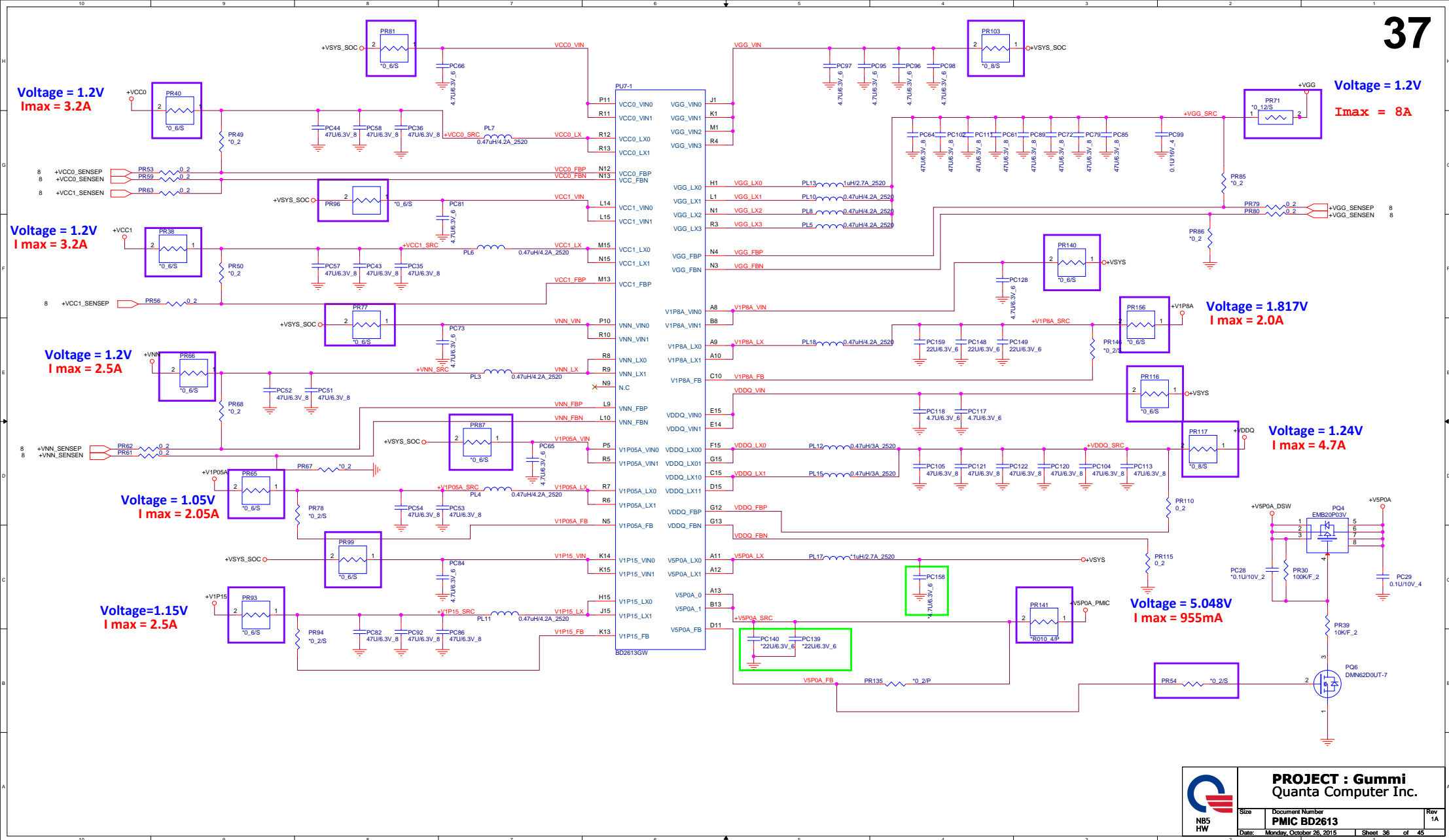


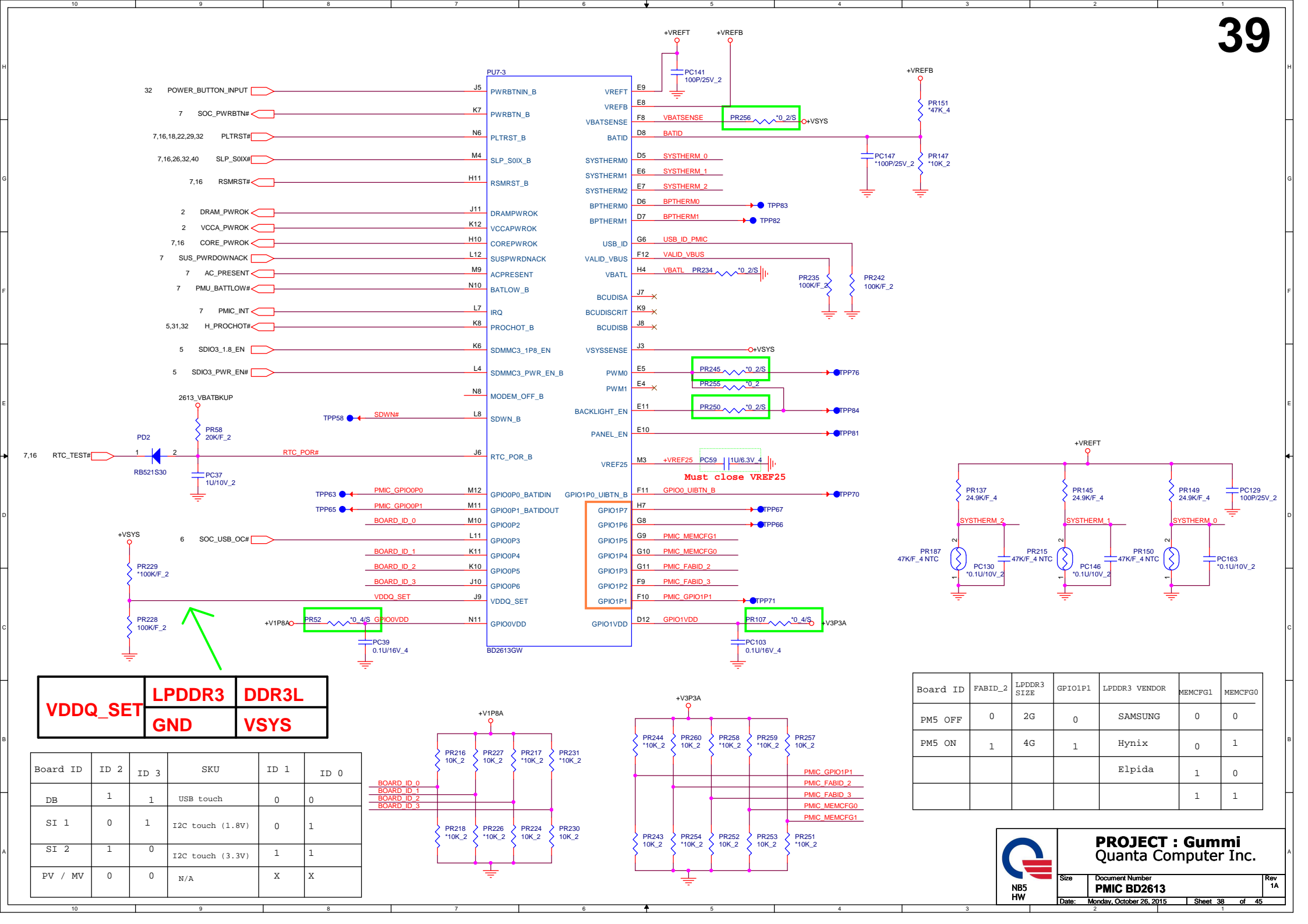
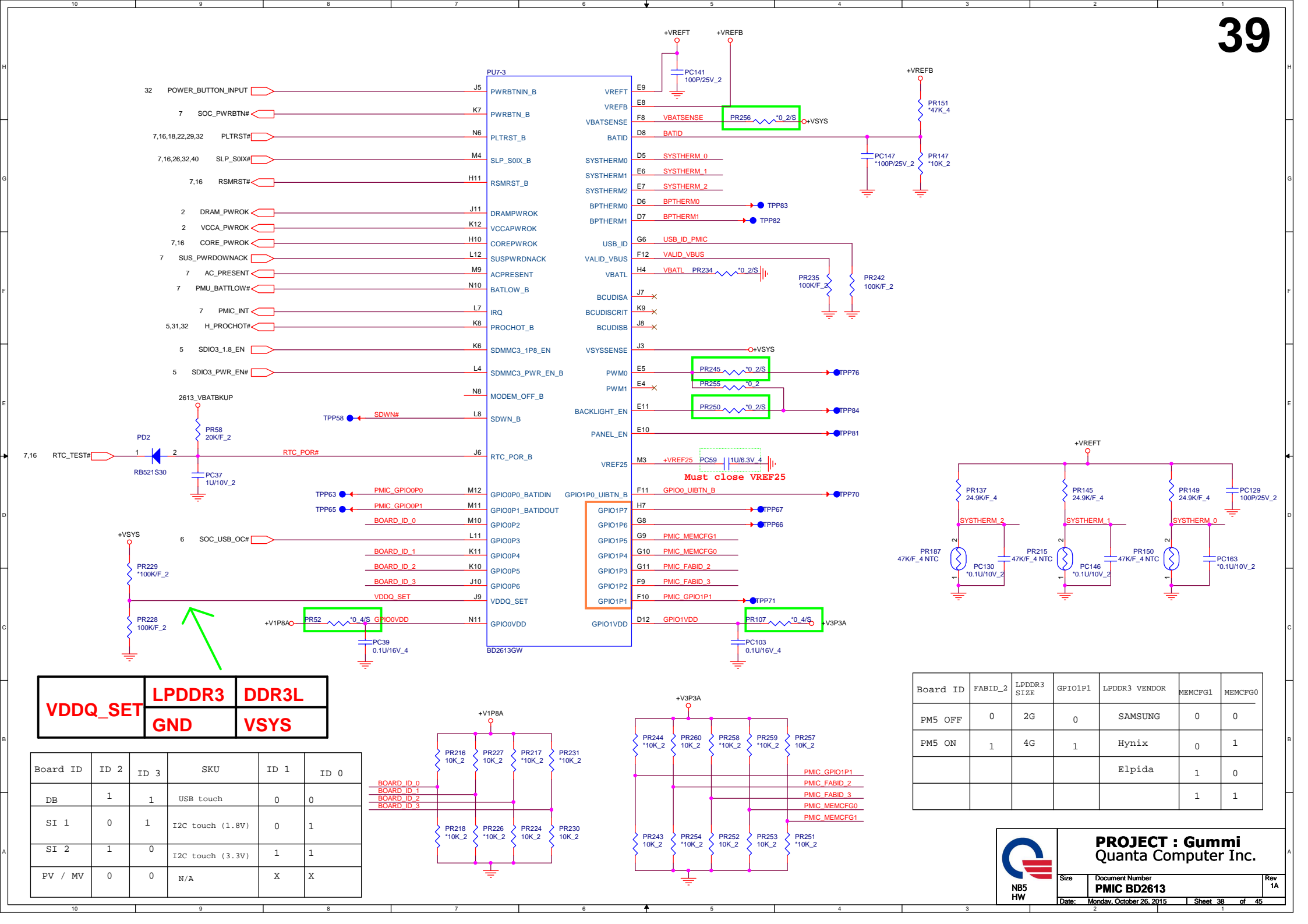


+3.3V +/- 5%
Countinue current:1.5A
OCp minimum:2A









39

VDDQ_SET

Board ID	ID 2	ID 3	SKU	ID 1	ID 0
DB	1	1	USB touch	0	0
SI 1	0	1	I2C touch (1.8V)	0	1
SI 2	1	0	I2C touch (3.3V)	1	1
PV / MV	0	0	N/A	X	X

LPDDR3

DDR3L

GND

VSYS

Must close VREF25

Board ID	FABID_2	LPDDR3 SIZE	GPIO1P1	LPDDR3 VENDOR	MEMCFG1	MEMCFG0
PM5 OFF	0	2G	0	SAMSUNG	0	0
PM5 ON	1	4G	1	Hynix	0	1
				Elpida	1	0
					1	1

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Quanta Computer Inc.

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VDDQ_SET

Board ID	ID 2	ID 3	SKU	ID 1	ID 0
DB	1	1	USB touch	0	0
SI 1	0	1	I2C touch (1.8V)	0	1
SI 2	1	0	I2C touch (3.3V)	1	1
PV / MV	0	0	N/A	X	X

LPDDR3

DDR3L

GND

VSYS

Must close VREF25

Board ID	FABID_2	LPDDR3 SIZE	GPIO1P1	LPDDR3 VENDOR	MEMCFG1	MEMCFG0
PM5 OFF	0	2G	0	SAMSUNG	0	0
PM5 ON	1	4G	1	Hynix	0	1
				Elpida	1	0
					1	1

PROJECT : Gummi
Quanta Computer Inc.

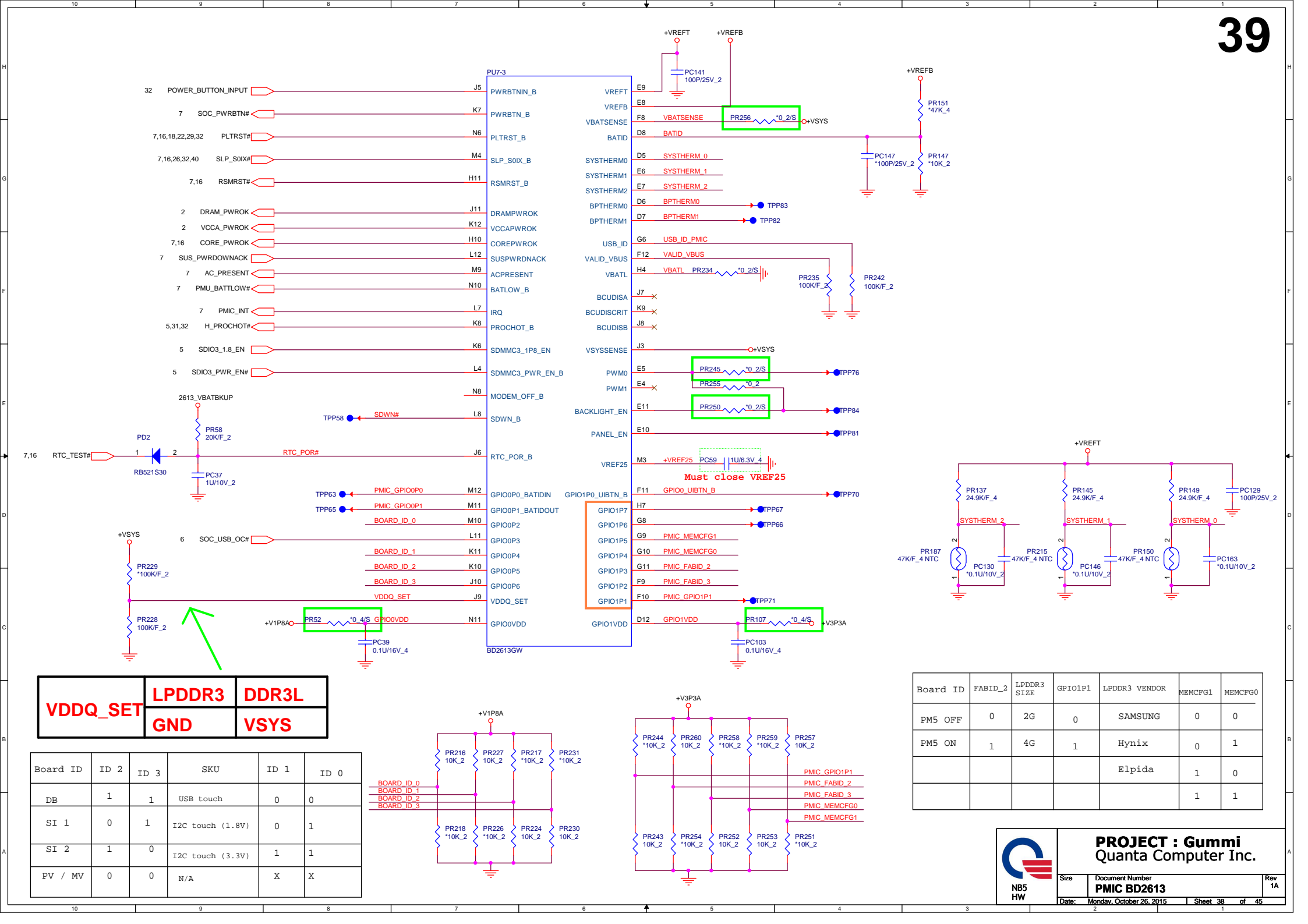
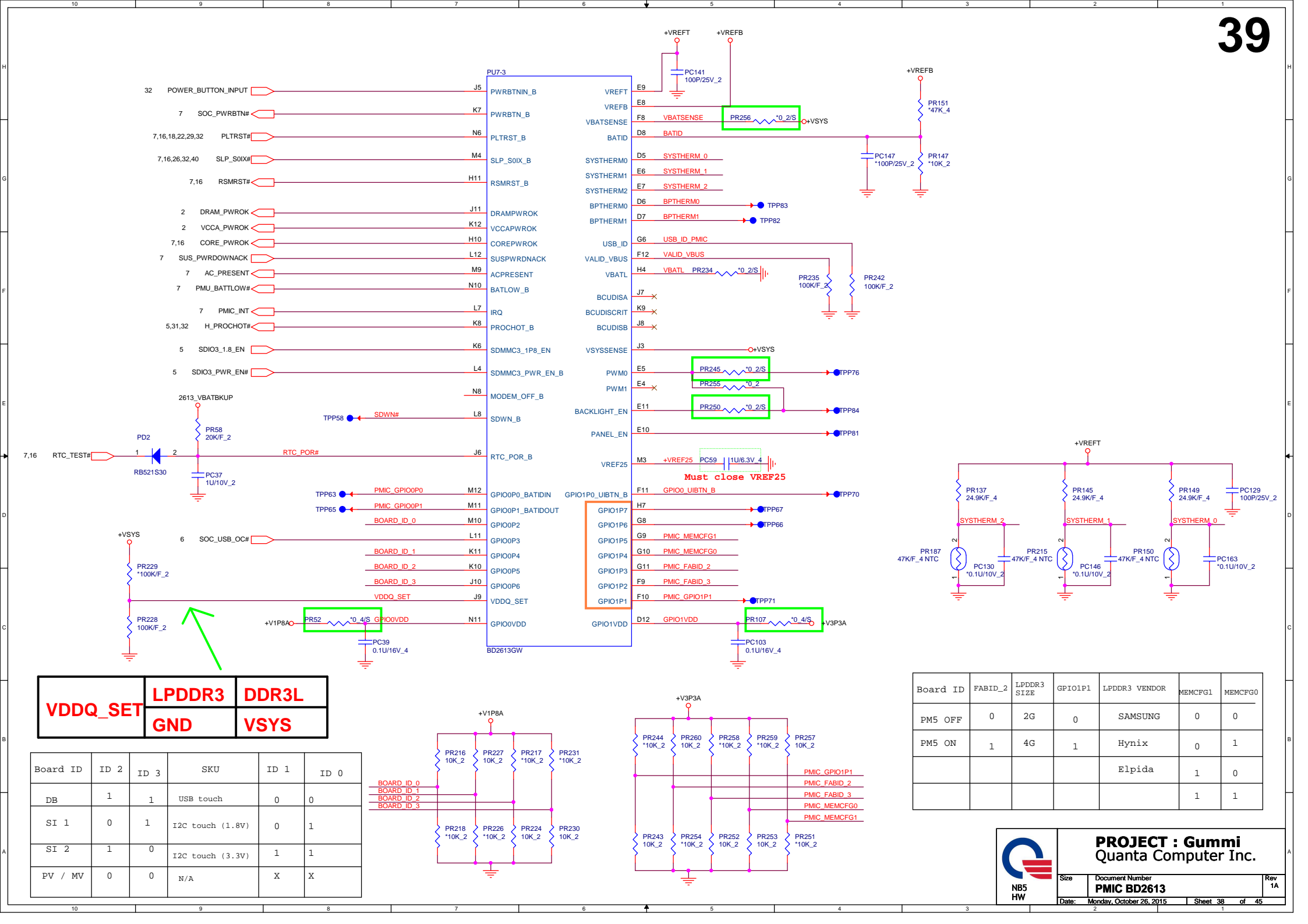
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VDDQ_SET

Board ID	ID 2	ID 3	SKU	ID 1	ID 0
DB	1	1	USB touch	0	0
SI 1	0	1	I2C touch (1.8V)	0	1
SI 2	1	0	I2C touch (3.3V)	1	1
PV / MV	0	0	N/A	X	X

PMIC BD2613

Board ID	FABID_2	LPDDR3 SIZE	GPIO1P1	LPDDR3 VENDOR	MEMCFG1	MEMCFG0
PM5 OFF	0	2G	0	SAMSUNG	0	0
PM5 ON	1	4G	1	Hynix	0	1
				Elpida	1	0
					1	1

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VDDQ_SET

Board ID	ID 2	ID 3	SKU	ID 1	ID 0
DB	1	1	USB touch	0	0
SI 1	0	1	I2C touch (1.8V)	0	1
SI 2	1	0	I2C touch (3.3V)	1	1
PV / MV	0	0	N/A	X	X

PMIC BD2613

Board ID	FABID_2	LPDDR3 SIZE	GPIO1P1	LPDDR3 VENDOR	MEMCFG1	MEMCFG0
PM5 OFF	0	2G	0	SAMSUNG	0	0
PM5 ON	1	4G	1	Hynix	0	1
				Elpida	1	0
					1	1

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